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Z-GRID SIGNAL CONDITIONING
SYSTEM USER'S GUIDE AND
TECHNICAL REFERENCE

M. J. Gutman

University of Dayton Research Institute
Electronic and Computer Development Laboratory
300 College Park Avenue
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May 1989

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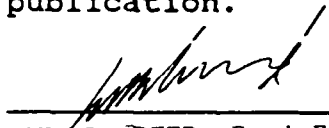
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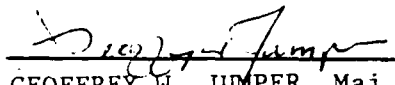
This technical report has been reviewed and is approved for publication.



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19. ABSTRACT (Continue on reverse if necessary and identify by block number) This report provides design and technical documentation on the electronic processing of vibration signals from the Z-Grid method of noncontacting blade tip deflection sensing. The Z-Grid system is used as an analytical tool for structural testing and research on bladed disk components. The signal conditioning system is designed to process two frequency modulated waveforms generated by the relative motion of a permanent magnet with respect to a conducting wire grid.						
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This report provides design and technical documentation on the Z-Grid Signal Processing System developed by the Electronic and Computer Development Laboratory, within the University of Dayton Research Institute. It was designed and fabricated as an analytical tool for structural testing and research on turbine components. This research effort was performed for the Aerospace Mechanics Group of the Research Institute. Michael Drake was the Principal Investigator and Robert Dominic was the Research Engineer in charge of daily activities for this project.

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1. GENERAL DESCRIPTION

This document provides technical information on the Z-Grid Signal Processing System. The system is designed to process two frequency modulated waveforms. These waveforms are generated by the motion of a magnet mounted on the blade of the rotating turbine past two wire grid rings. The individual conductors of the two grids are insulated from each other and crossed orthogonally. The rotation of the blade produces variable phase differences between the two FM signals, dependent on the location of the magnet as it passes under the series of paired crossing conductors of the grid. The signal processing system processes this waveform and generates pulses corresponding to the peaks of the input waveform. Figure 1 shows sections of the system input and output waveforms.

The output pulses drive an LED for each of the two grid channels. The LEDs replace two of the fiber-optic bundles on the NSMS Data Acquisition System. The grid data is then acquired using the procedures described in the NSMS manuals. A general block diagram of the Z-Grid Signal Processing System is shown in Figure 2.

System input and output test cards are supplied and stored in the spare slots of the Z-Grid Signal Processor rack. Figure 3 shows the use of these cards in the system test mode. The test cards are for diagnostic and troubleshooting purposes, and should be used by qualified personnel only.

2. SYSTEM CONNECTIONS

The Z-Grid Signal Processing System is designed to be used in conjunction with the NSMS data acquisition system. The Z-Grid system provides its output using high intensity LED's which are substituted for the fiber-optic cables in the NSMS system. The only other connection required is a BNC cable from the grids to the preamplifiers for the Signal Conditioning System.

3. SYSTEM OPERATION

The Z-Grid Signal Processing System is designed to be transparent to the user of the NSMS System. All procedures for operating the system are identical to those described in the Turbine Blade Data Acquisition System User's Guide, with the following exception:

The number of blades (see WRDC-TR-87-2041, Page 5, section 9), must be equal to the number of crossings on the grid.

At the time of this writing, this number must be set to 36.

There are two adjustments on the Z-Grid Signal Processing System. These are the Preamp Level and the Time Delay adjustment. These should be adjusted when a new speed is used or the configuration of the system is changed (i.e. a new turbine, magnet or grid is installed on the system).

The Preamp Level must be adjusted for an output level between one-half and one volt. This is adjusted while the system is running at speed using pot R6 on the Preamp Board (Figure 7).

The Time Delay adjustment is determined by the setting of switches DS7 and DS8 on the Output Card (Figure 22). The Time Delay may be set based on either the turbine rotation speed or the desired time delay after the input pulse peak. Table 1 provides the information necessary for setting the delay.

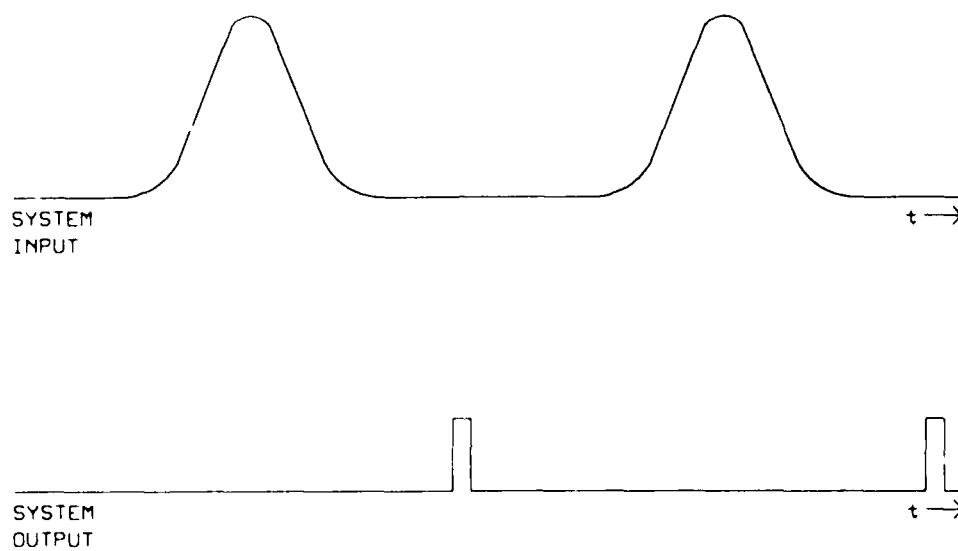
The first method of setting the delay is to set the switches based on a given turbine rotation speed. Using this method, locate the speed at which the turbine will be run in the "RPM" column of Table 1 and set switches DS7 and DS8 on the Output Card using the binary switch column of the table. Be sure to note the correct orientation of the MSB and LSB on the switches. Refer to Figure 22 for the location of the MSB and LSB.

The second method of setting the delay is to set the switches for a specific delay time. Choose a delay time from the "DELAY TIME" column on Table 1 and set the switches accordingly. The time delay is shown in Figure 4 as the Delay Time. The output pulse must fall within the period labeled WT on Figure 4, for correct system operation. It is important to insure that the delay time is not so short that the output pulse occurs during the pulse on the input waveform, nor so long that the output pulse occurs during the next pulse on the input waveform.

In addition to the two routine adjustments on the Z-Grid Signal Processing System, the characteristics of the digital filter can also be changed. This provision is included for reasons of system versatility. Table 2 lists the filter coefficients for use with different numbers of filter stages. The resulting filter characteristics are plotted in Figure 5. The filter coefficients **do not** require routine adjustment. The setting of the filter coefficients should not be attempted by unqualified personnel.

4. SYSTEM ELECTRONICS

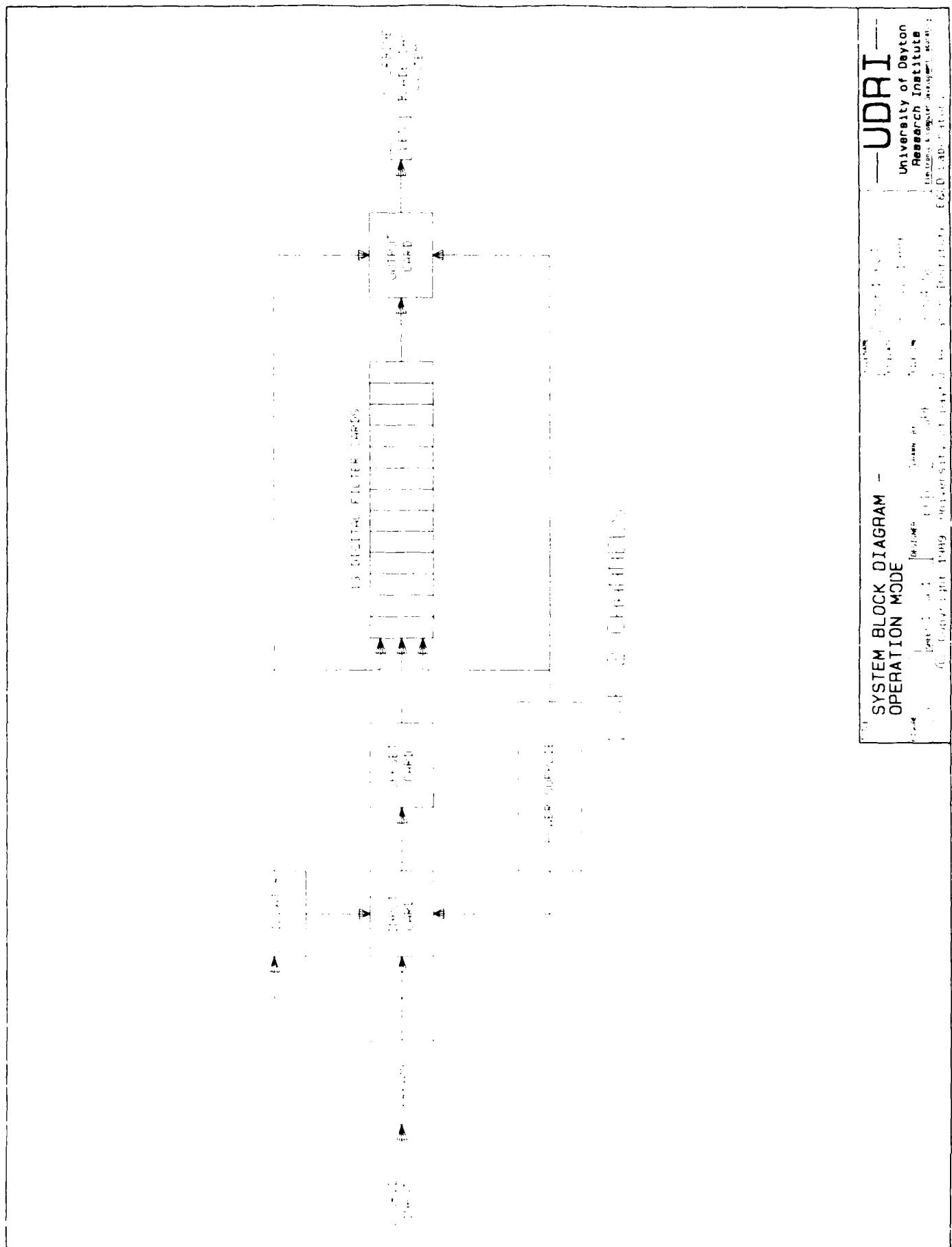
Figures 6a and 6b are an expanded version of the system block diagram in Figure 1. Figures 6a and 6b show the path of the input signal through the Signal Processor electronic circuitry. The circuitry consists of an external preamplifier, a Signal Processor rack with backplane-mounted, removable circuit cards, system test cards, and two output LEDs. The electronic circuitry for the Z-Grid Signal Processing System is documented in Figures 7 through 38. Parts lists for the circuit components are provided in Tables 3 through 11. The backplane pinout for the Signal Processing rack is listed in Table 12. This information is a technical reference provided for ease of repair or troubleshooting of the system by qualified personnel only.



SYSTEM INPUT AND OUTPUT
WAVEFORMS

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Figure 1. System Input and Output Waveforms



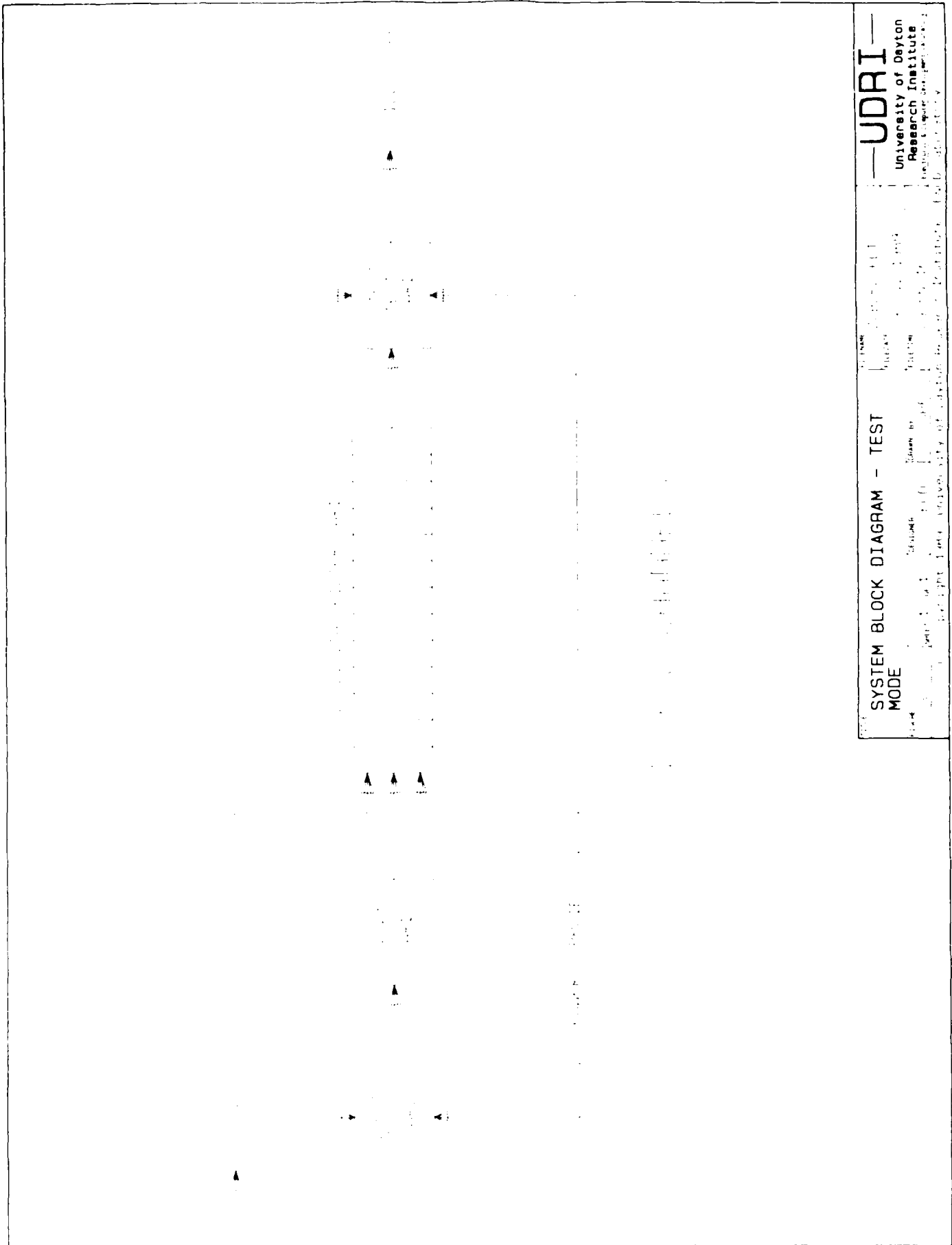
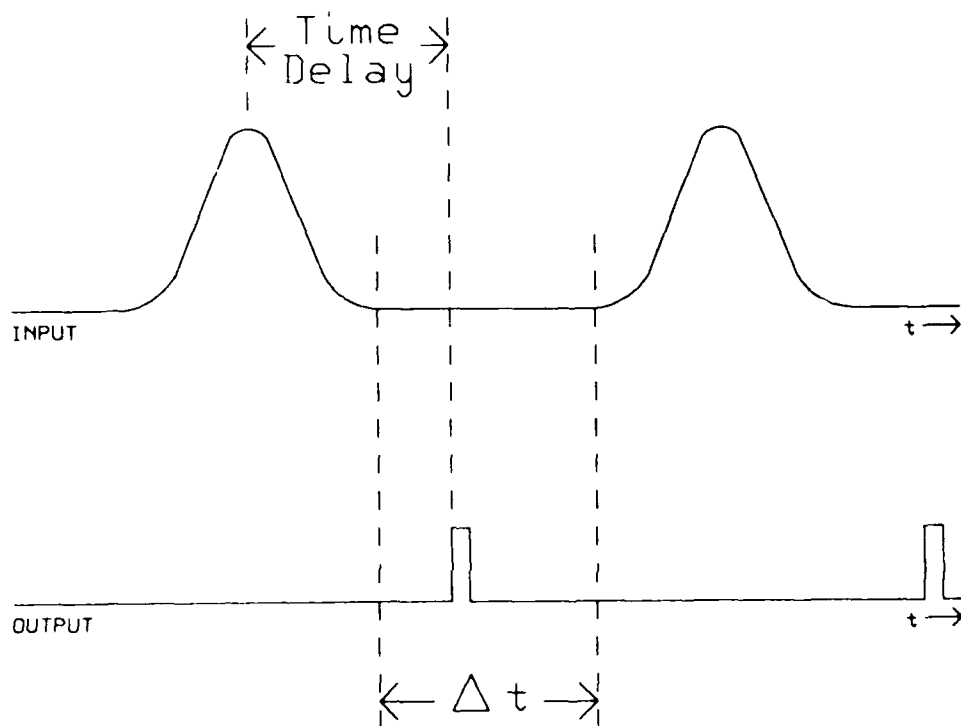


Figure 3. System Block Diagram - Test Mode

RPM	PERIOD (USEC)	DELAY TIME	COUNTS DECIMAL	COUNTS HEX	SWITCH SETTINGS	SWITCH (BINARY)
					MSB	LSB
1100	1515	758	3787	ecb	134	XXXX000100110100
1200	1389	694	3472	d90	26f	XXXX000100110111
1300	1282	641	3205	c85	37a	XXXX001101111010
1400	1190	595	2976	ba0	45f	XXXX010001011111
1500	1111	556	2777	ad9	526	XXXX010100100110
1600	1042	521	2604	a2c	5d3	XXXX010111010011
1700	980	490	2450	992	66d	XXXX011001101101
1800	926	463	2314	90a	6f5	XXXX011011110101
1900	877	439	2192	890	76f	XXXX011101101111
2000	833	417	2083	823	7dc	XXXX011110111100
2100	794	397	1984	7c0	83f	XXXX100000111111
2200	758	379	1893	765	89a	XXXX100010011010
2300	725	362	1811	713	8ec	XXXX100011101100
2400	694	347	1736	6c8	937	XXXX100100110111
2500	667	333	1666	682	97d	XXXX100101111101
2600	641	321	1602	642	9bd	XXXX100110111101
2700	617	309	1543	607	9f8	XXXX100111111000
2800	595	298	1488	5d0	a2f	XXXX101000101111
2900	575	287	1436	59c	a63	XXXX101001100011
3000	556	278	1388	56c	a93	XXXX101010010011
3100	538	269	1344	540	abf	XXXX101010111111
3200	521	260	1302	516	ae9	XXXX101011101001
3300	505	253	1262	4ee	b11	XXXX101100010001
3400	490	245	1225	4c9	b36	XXXX101100110110
3500	476	238	1190	4a6	b59	XXXX101101011001
3600	463	231	1157	485	b7a	XXXX101101111010
3700	450	225	1126	466	b99	XXXX101110011001
3800	439	219	1096	448	bb7	XXXX101110110111
3900	427	214	1068	42c	bd3	XXXX101111010011
4000	417	208	1041	411	bee	XXXX101111101110
4100	407	203	1016	3f8	c07	XXXX110000000111
4200	397	198	992	3e0	c1f	XXXX110000011111
4300	388	194	968	3c8	c37	XXXX110000110111
4400	379	189	946	3b2	c4d	XXXX110001001101
4500	370	185	925	39d	c62	XXXX110001100010
4600	362	181	905	389	c76	XXXX110001110110
4700	355	177	886	376	c89	XXXX110010001001
4800	347	174	868	364	c9b	XXXX110010011011
4900	340	170	850	352	cad	XXXX110010101101
5000	333	167	833	341	cbe	XXXX110010111110
5100	327	163	816	330	ccf	XXXX110011001111
5200	321	160	801	321	cde	XXXX110011011110
5300	314	157	786	312	ced	XXXX110011101101
5400	309	154	771	303	cfc	XXXX110011111100
5500	303	152	757	2f5	d0a	XXXX110100001010
5600	298	149	744	2e8	d17	XXXX110100010111
5700	292	146	730	2da	d25	XXXX110100100101
5800	287	144	718	2ce	d31	XXXX110100110001
5900	282	141	706	2c2	d3d	XXXX110100111101
6000	278	139	694	2b6	d49	XXXX110101001001

DIP Switch settings: 1 = open; 0 = closed; X = don't care

Table 1. Time Delay Settings



NOTES:

- 1) OUTPUT PULSES MUST FALL WITHIN THE Δt INTERVALS

TIME DELAY SETTING RANGE

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Figure 4. Time Delay Setting Range

ZGRID SYSTEM FILTER COEFFICIENTS

10/24/88

STAGE#	7	13	15
1	-1 11111111 SIGNED	-11 11110101 SIGNED	-78 10110010 SIGNED
2	4 00000100 SIGNED	0 00000000 SIGNED	-13 11110011 SIGNED
3	11 00001011 SIGNED	9 00001001 SIGNED	42 00101010 SIGNED
4	14 00001110 SIGNED	16 00010000 SIGNED	87 01010111 SIGNED
5	11 00001011 SIGNED	21 00010101 SIGNED	122 01111010 SIGNED
6	4 00000100 SIGNED	24 00011000 SIGNED	147 10010011 UNSIGNED
7	-1 11111111 SIGNED	25 00011001 SIGNED	162 10100010 UNSIGNED
8		24 00011000 SIGNED	167 10100111 UNSIGNED
9		21 00010101 SIGNED	162 10100010 UNSIGNED
10		16 00010000 SIGNED	147 10010011 UNSIGNED
11		9 00001001 SIGNED	122 01111010 SIGNED
12		0 00000000 SIGNED	87 01010111 SIGNED
13		-11 11110101 SIGNED	42 00101010 SIGNED
14			-13 11110011 SIGNED
15			-78 10110010 SIGNED
SUM	42	143	1105

Table 2. Filter Coefficient Data

ZGRID FILTER COEFFICIENTS

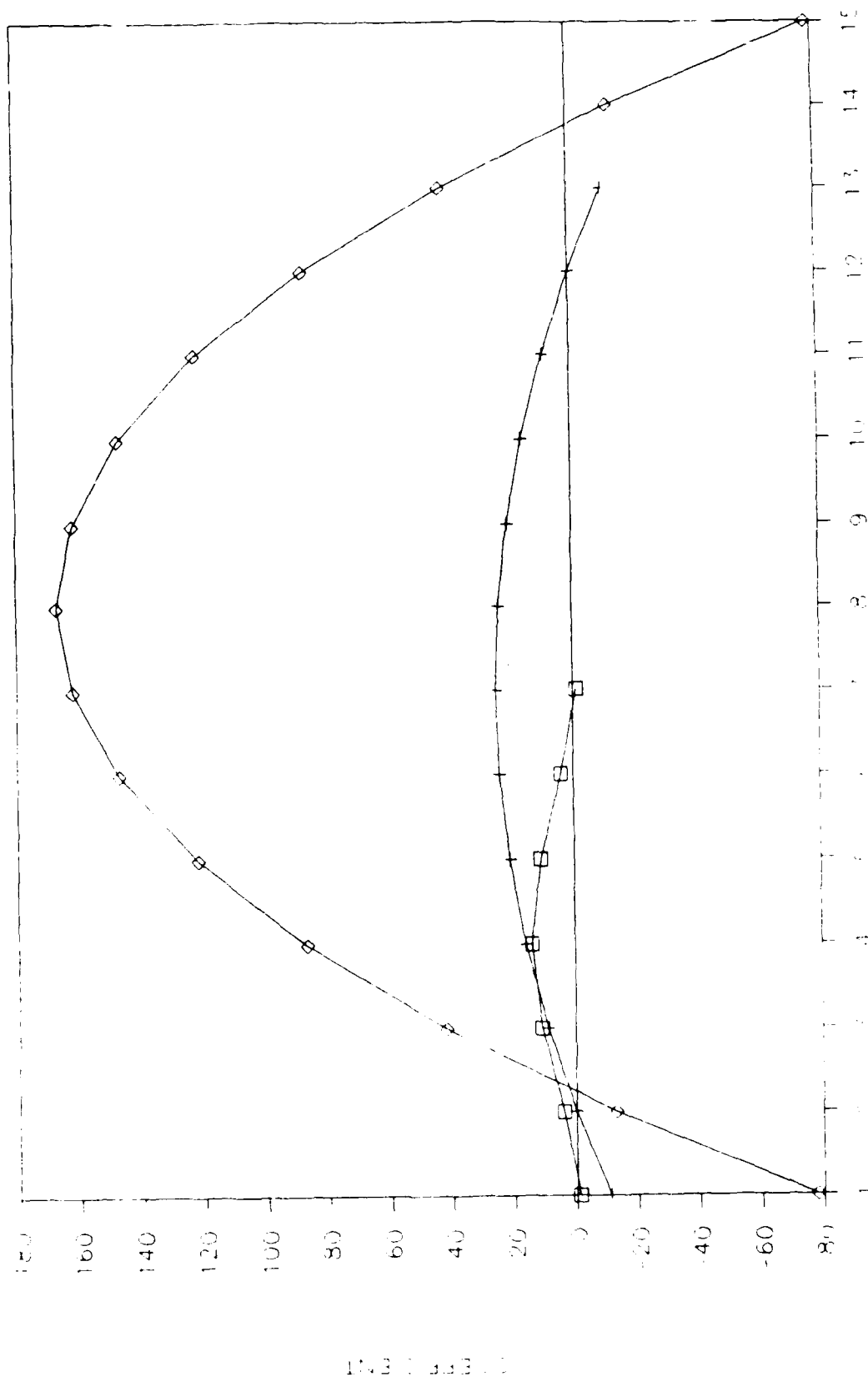


FIGURE 5

Figure 5. Filter Coefficient Curves

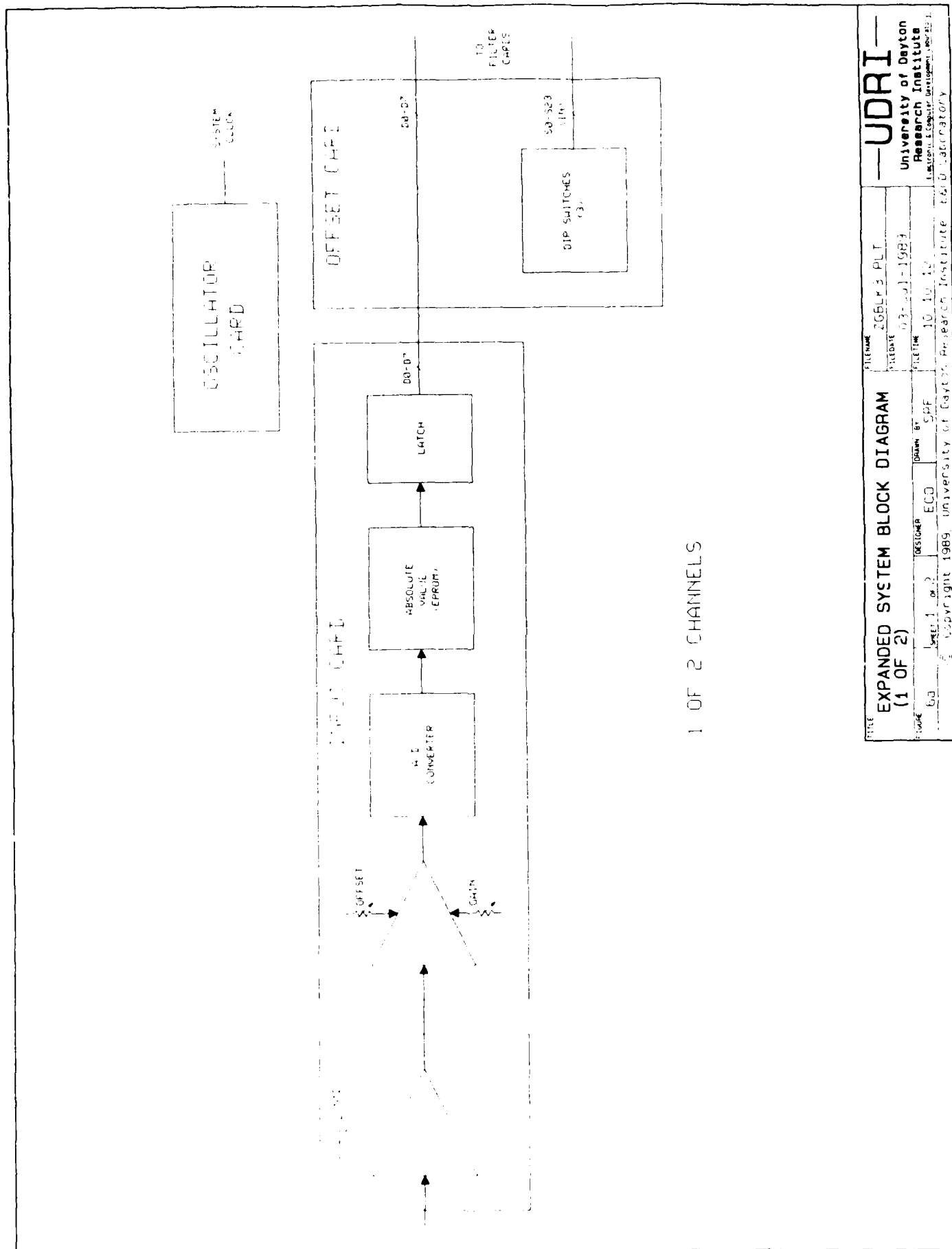


Figure 6a. Expanded System Block Diagram (1 of 2)

FILE		EXPANDED SYSTEM BLOCK DIAGRAM (1 OF 2)		SHEET 265LX3 PL1		UDRI	
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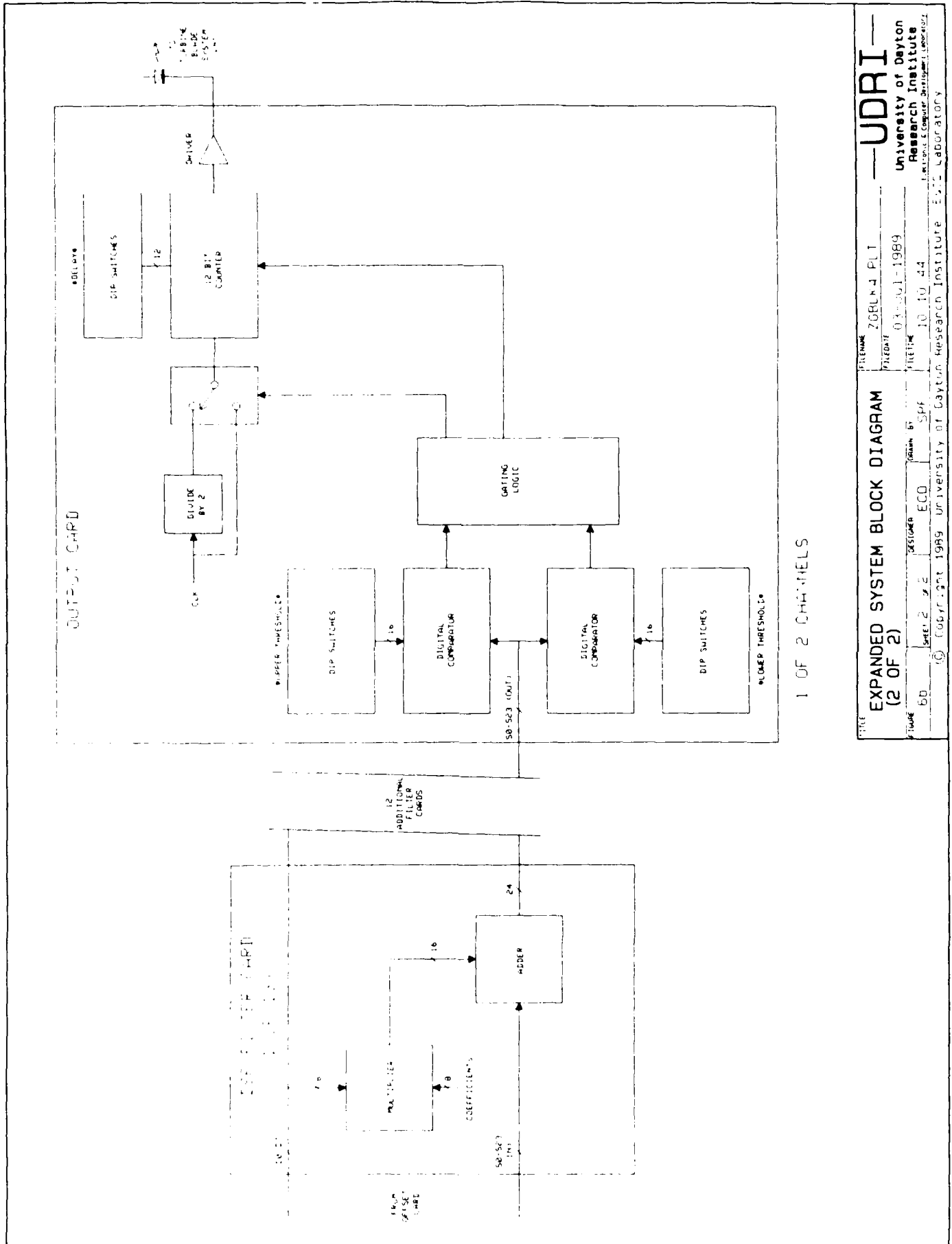
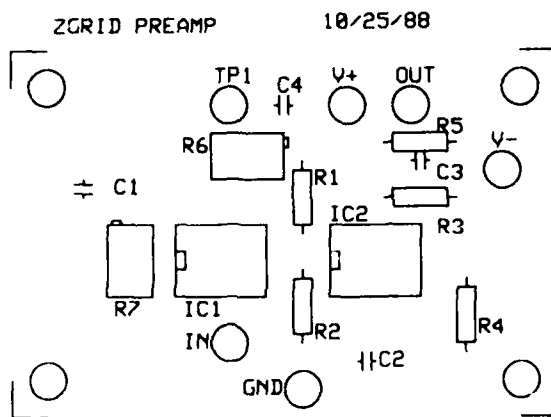


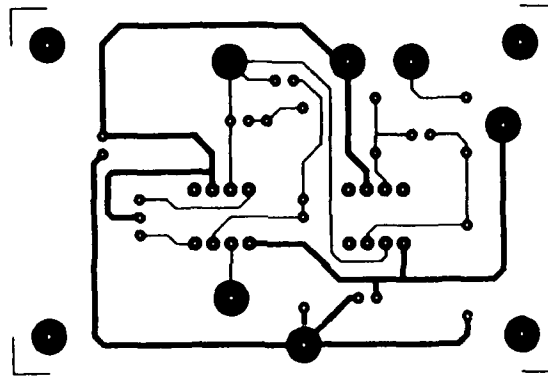
Figure 6b. Expanded System Block Diagram (2 of 2)



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Figure 7. Preamp Board Component Layout

SOLDER



PREAMP BOARD ARTWORK

DATE: 11-04-93
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CHECKED BY: [blank]

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Electronic & Computer Development Laboratory

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Figure 8. Preamp Board Artwork
14

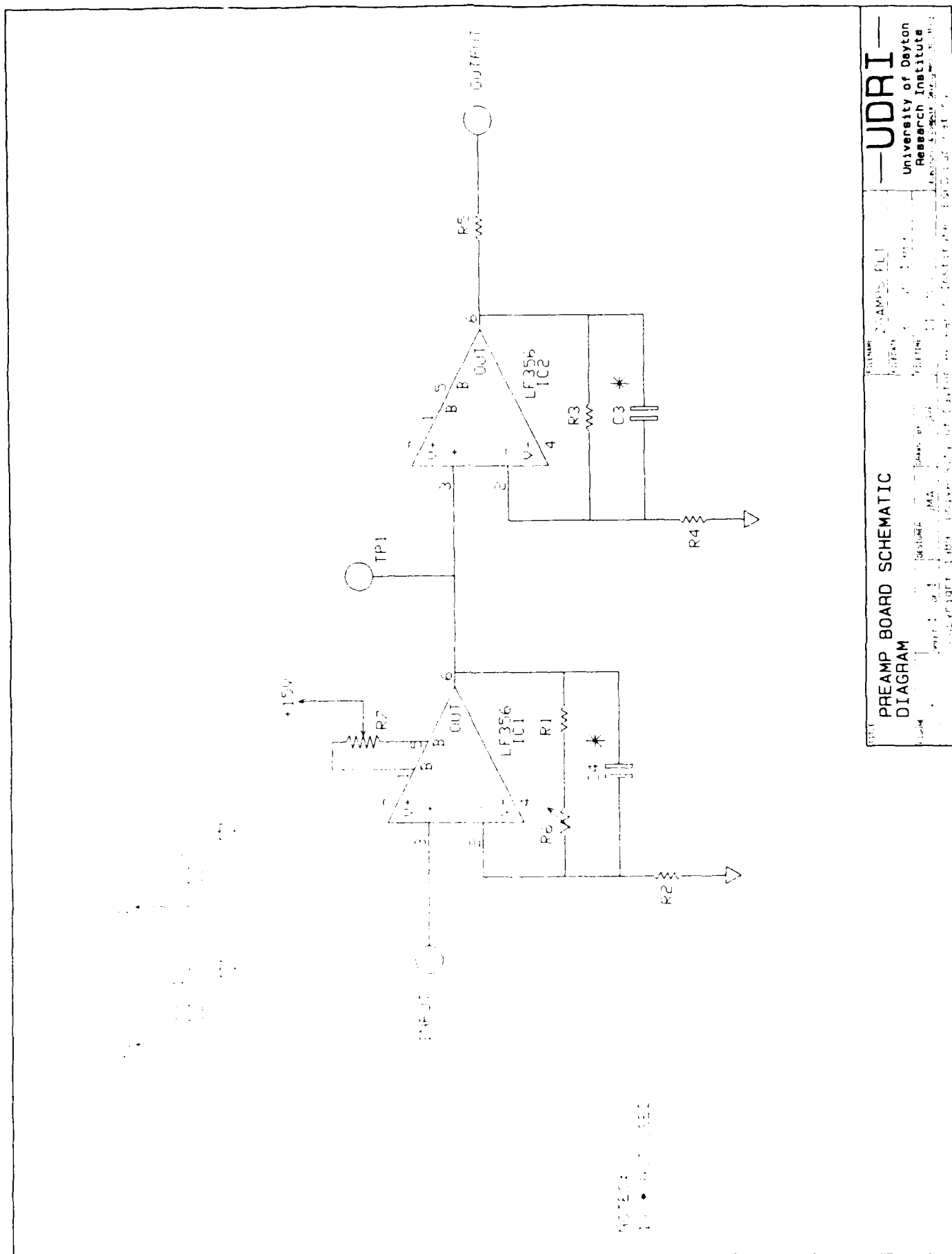


Figure 9. Preamp Board Schematic Diagram

Z-GRID PREAMP BOARD PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	5% tol., 1/4 W resistor	330 Ohm
R2	5% tol., 1/4 W resistor	330 Ohm
R3	1% tol., 1/4 W resistor	750 Ohm
R4	5% tol., 1/4 W resistor	100 Ohm
R5	5% tol., 1/4 W resistor	110 Ohm
R6	10 turn PCB trimpot	1k Ohm Bourns type 3299
R7	10 turn PCB trimpot	10k Ohm Bourns type 3299
C1	monocap	0.1 uF
C2	monocap	0.1 uF
C3	not used	
C4	not used	
IC1	JFET operational amp.	LF356 (National)
IC2	JFET operational amp.	LF356 (National)

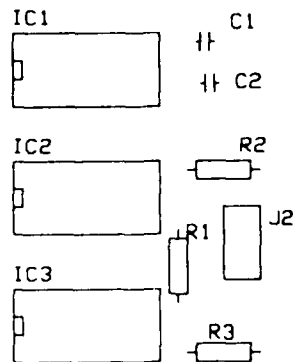
Table 3. Preamp Board Parts List

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Z GRID DIGITAL SIGNAL PROCESSOR

UNIVERSITY OF DAYTON RESEARCH INSTITUTE

J3



J1

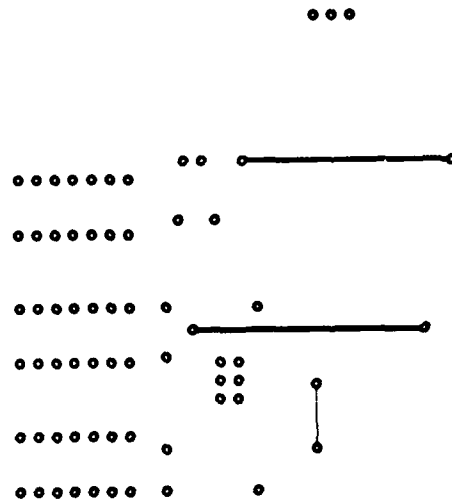
OSCILLATOR CARD COMPONENT
LAYOUT

DATE: 11/21/88
BY: [illegible]
CHECKED: [illegible]
APPROVED: [illegible]

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Figure 10. Oscillator Card Component Layout

COMPONENT



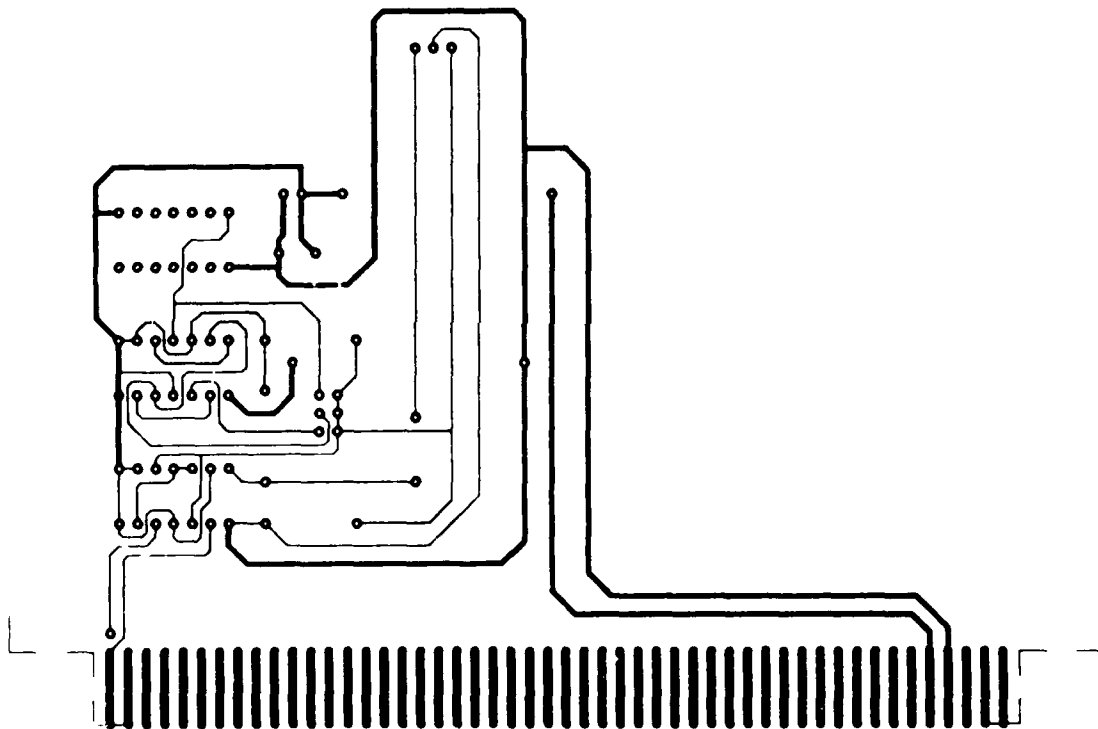
OSCILLATOR CARD ARTWORK
(COMPONENT SIDE)

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Figure 11. Oscillator Card Artwork (Component Side)

SOLDER



OSCILLATOR CARD ARTWORK
(SOLDER SIDE)

DATE: 11-01-76
BY: J. L. B. 11-01-76
CHECKED: 11-01-76
APPROVED: 11-01-76

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Figure 12. Oscillator Card Artwork (Solder Side)

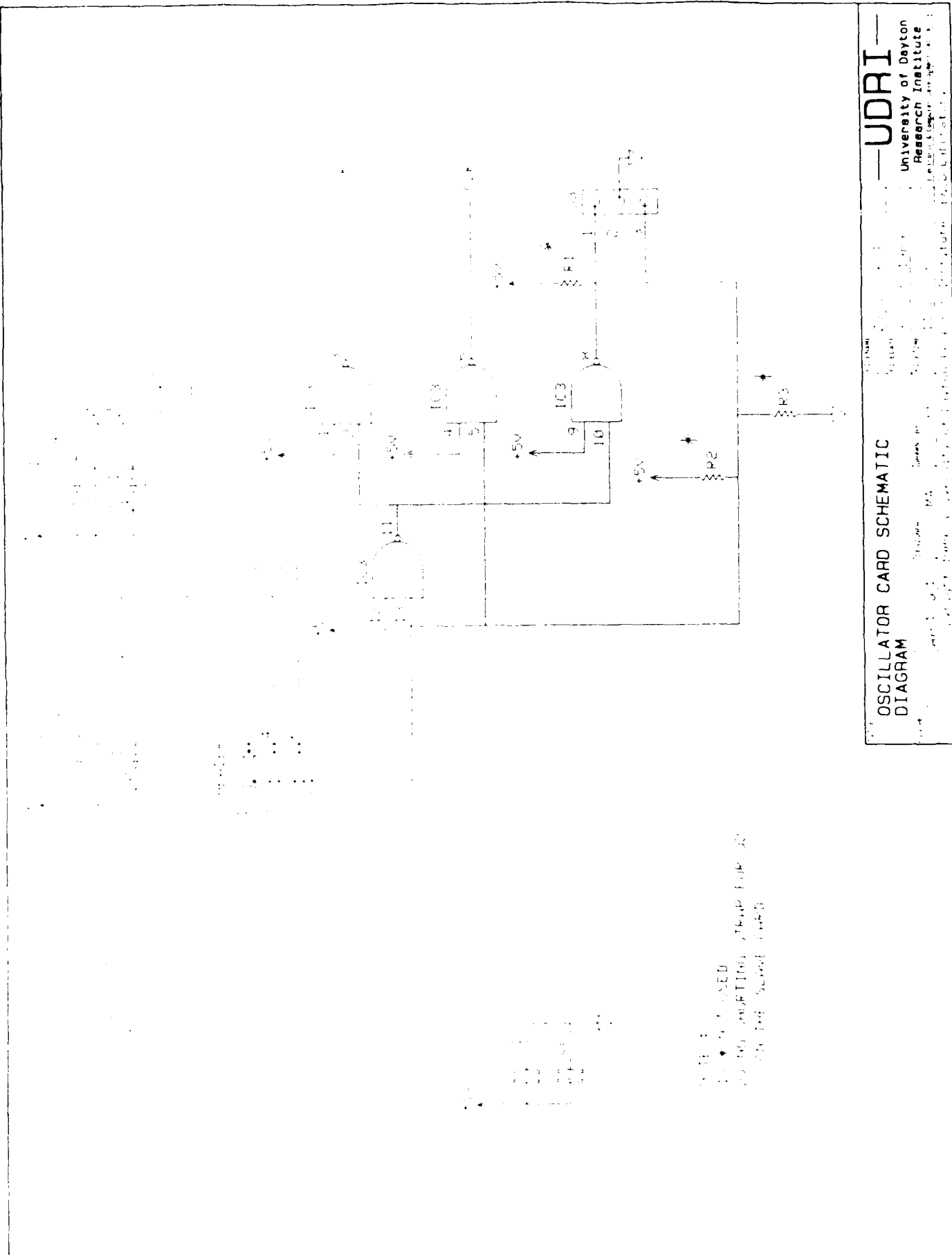


Figure 13. Oscillator Card Schematic Diagram

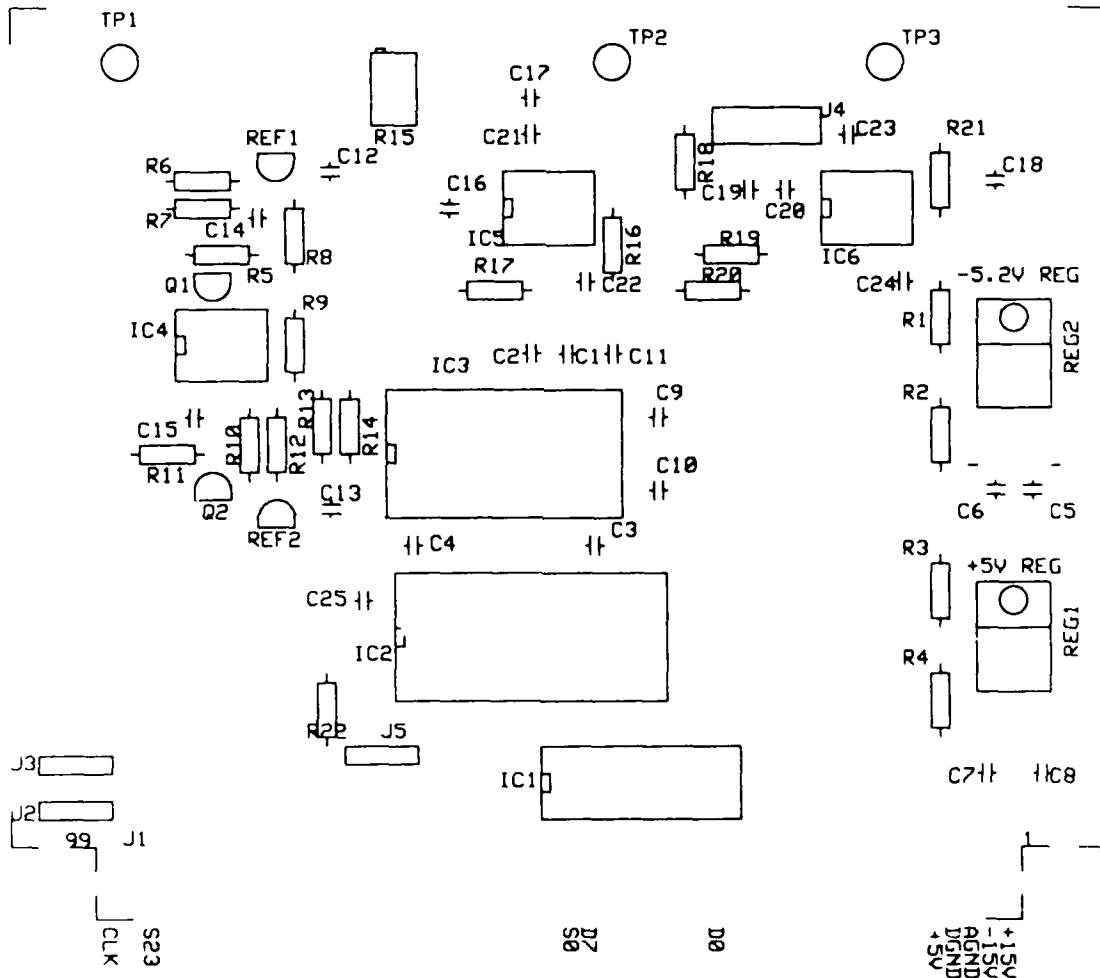
Z-GRID OSCILLATOR CARD PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	not used	
R2	not used	
R3	not used	
C1	monocap	0.1 uF
C2	monocap	0.1 uF
IC1	5.000 MHz oscillator	Digi-key, X109
IC2	dual D flip-flop	74LS74 (National)
IC3	quad 2-input NAND buffer	7437 (National)
J2	dual row PCB header	6 pin, .1" spacing
J3	single row PCB header	3 pin, .1" spacing

Table 4. Oscillator Card Parts List

Z GRID DIGITAL SIGNAL PROCESSOR

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INPUT CARD COMPONENT LAYOUT

FILENAME: ZGTPD B.T.
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Figure 14. Input Card Component Layout

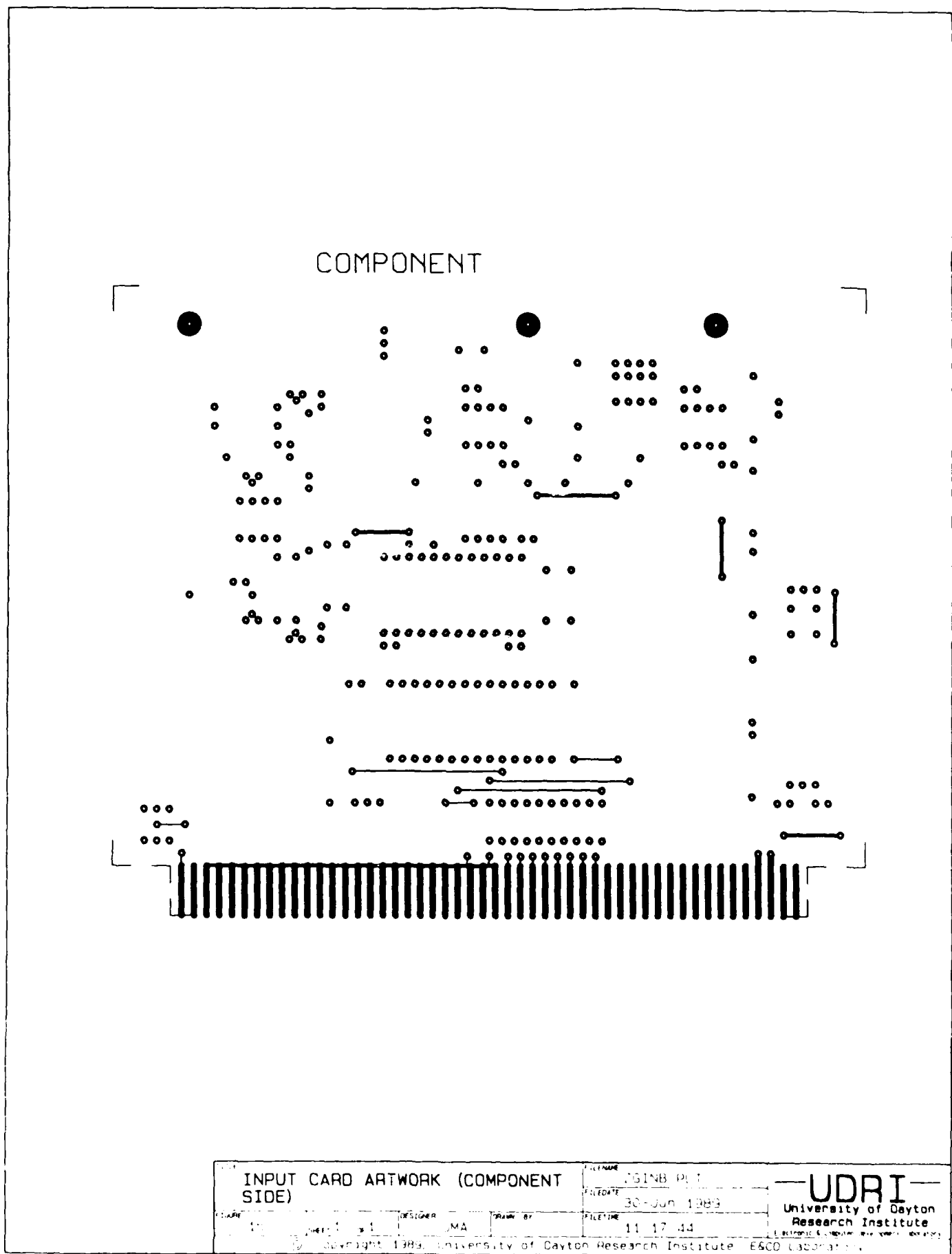
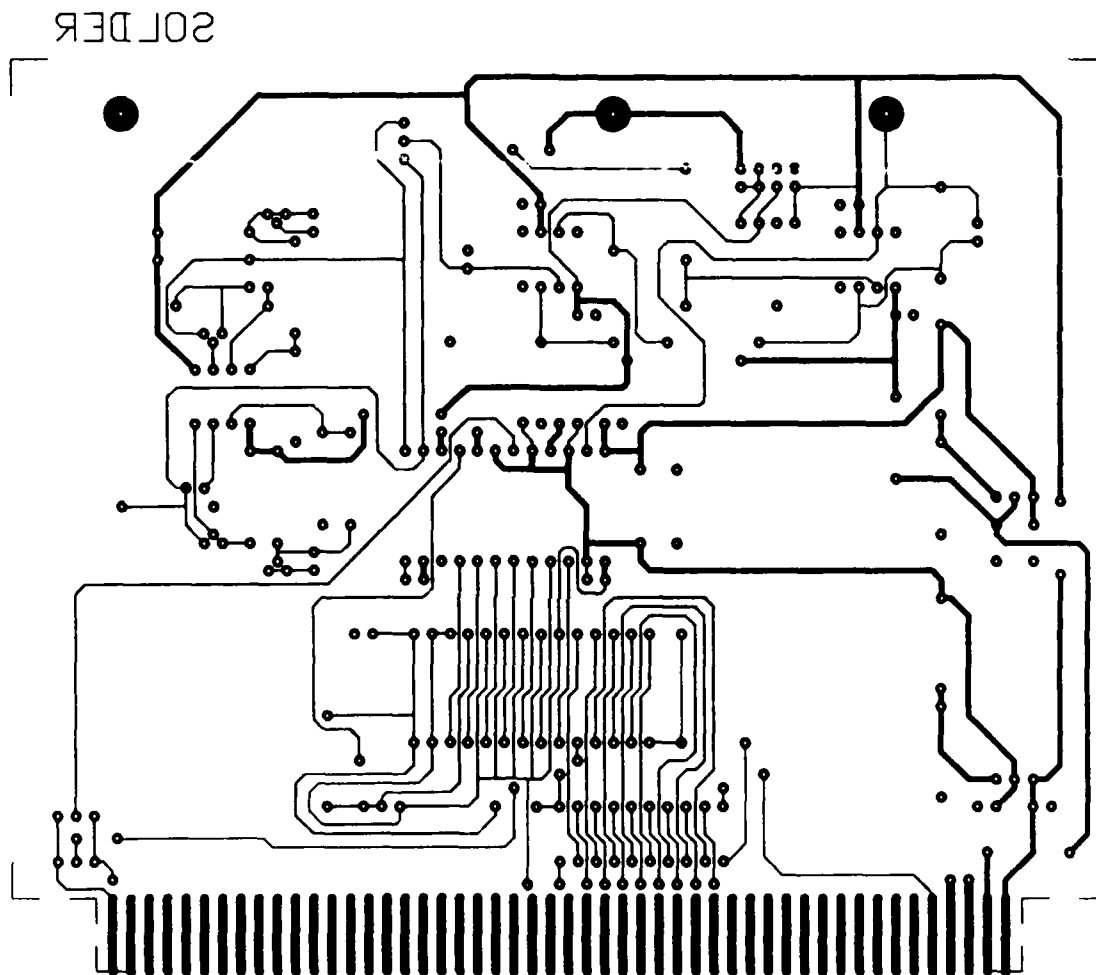


Figure 15. Input Card Artwork (Component Side)



INPUT CARD ARTWORK (SOLDER SIDE)				FILENAME	201NA.PLT	UDRI University of Dayton Research Institute <small>ELECTRONIC & MECHANICAL DESIGN GROUP</small>
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				FILETIME	11:13:50	
FIGURE	10	SHEET 1 of 1	DESIGNER	MA	DESIGNED BY	
Copyright 1989, University of Dayton Research Institute. EACD Laboratory.						

Figure 16. Input Card Artwork (Solder Side)

INPUT CARD SCHEMATIC DIAGRAM

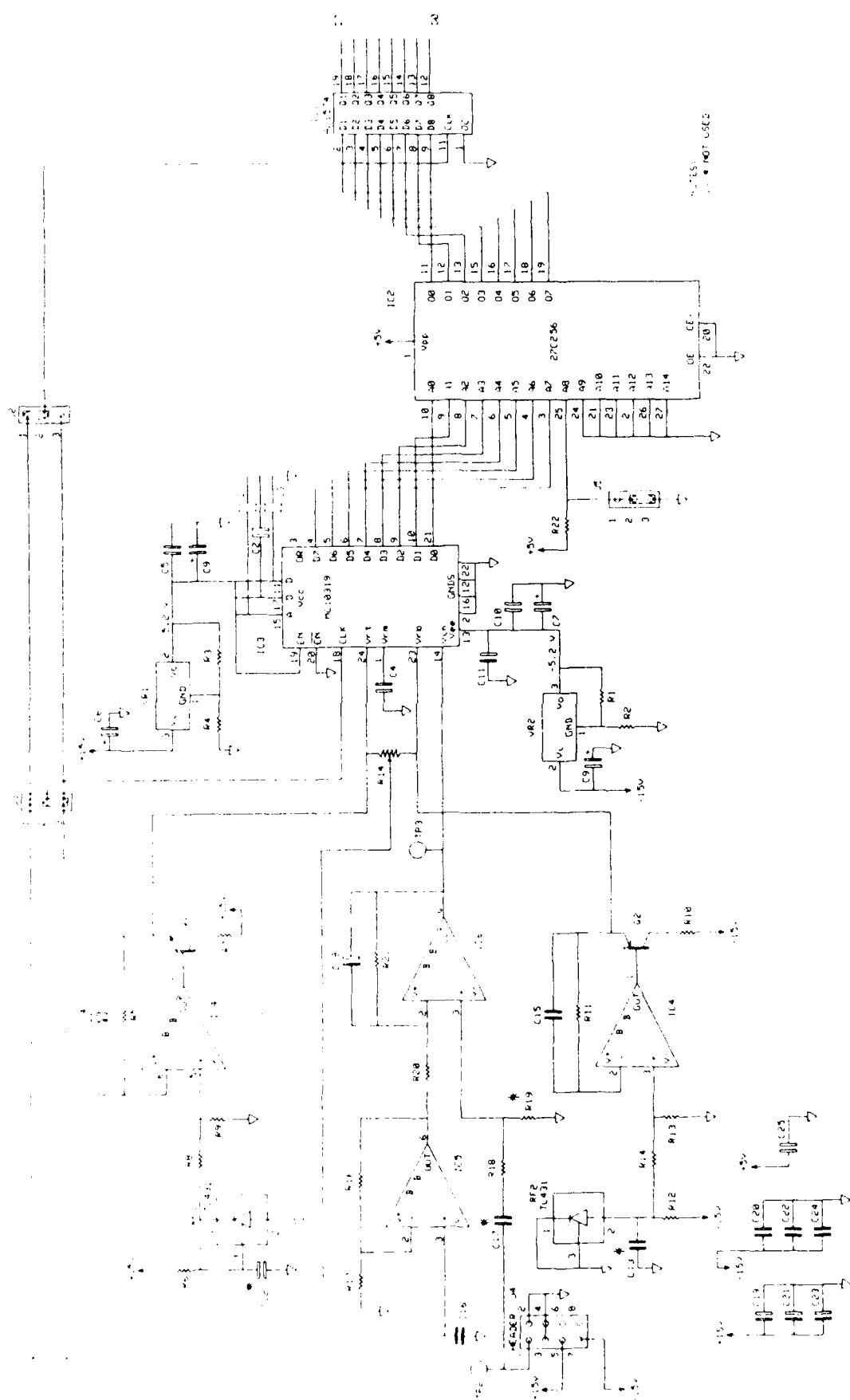


Figure 17. Input Card Schematic Diagram
25

Z-GRID INPUT CARD PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	1% tol., 1/4 W resistor	237 Ohm
R2	1% tol., 1/4 W resistor	750 Ohm
R3	1% tol., 1/4 W resistor	237 Ohm
R4	1% tol., 1/4 W resistor	750 Ohm
R5	5% tol., 1/2 W resistor	680 Ohm
R6	1% tol., 1/4 W resistor	2.15K Ohm
R7	5% tol., 1/2 W resistor	680 Ohm
R8	1% tol., 1/4 W resistor	1.5K Ohm
R9	1% tol., 1/4 W resistor	1K Ohm
R10	5% tol., 1/2 W resistor	680 Ohm
R11	5% tol., 1/2 W resistor	680 Ohm
R12	1% tol., 1/4 W resistor	2.15K Ohm
R13	1% tol., 1/4 W resistor	1K Ohm
R14	1% tol., 1/4 W resistor	1.5K Ohm
R15	10-turn PCB trimpot	10K Ohm Bourns type 3299
R16	1% tol., 1/4 W resistor	1K Ohm
R17	1% tol., 1/4 W resistor	1K Ohm
R18	1% tol., 1/4 W resistor	499 Ohm
R19	not used	
R20	1% tol., 1/4 W resistor	1K Ohm
R21	1% tol., 1/4 W resistor	2.15K Ohm
R22	1% tol., 1/4 W resistor	2.15K Ohm
C1	monocap	0.1 uF
C2	monocap	0.1 uF
C3	monocap	0.1 uF
C4	monocap	0.1 uF
C5	16V tantalum capacitor	10 uF
C6	16V tantalum capacitor	10 uF
C7	monocap	0.1 uF
C8	monocap	0.1 uF
C9	16V tantalum capacitor	10 uF
C10	16V tantalum capacitor	10 uF
C11	monocap	0.1 uF
C12	not used	
C13	not used	
C14	monocap	0.1 uF
C15	monocap	0.1 uF
C16	monocap	0.01 uF
C17	not used	replace with jumper
C18	disk capacitor	3.3 pF
C19	monocap	0.1 uF
C20	monocap	0.1 uF
C21	monocap	0.1 uF
C22	monocap	0.1 uF
C23	monocap	0.1 uF
C24	monocap	0.1 uF
C25	monocap	0.1 uF
Q1	nnp bipolar transistor	2N2222
Q2	npn bipolar transistor	2N2907
VR1	3-terminal regulator	LM317 (National)
VR2	3-terminal regulator	LM337 (National)
REF1	zener voltage reference	TL431 (Motorola)
REF2	zener voltage reference	TL431 (Motorola)
IC1	octal 3-state D flip-flop	74ALS574 (National)
IC2	EPROM	27C256 (National)
IC3	8-bit A/D converter	MC10319 (Motorola)
IC4	dual JFET op-amp.	MC34002 (Motorola)
IC5	JrET operational amp.	MC34080 (Motorola)
IC6	JFET operational amp.	MC34080 (Motorola)
J2	single row PCB header	3 pin, .1" spacing
J3	single row PCB header	3 pin, .1" spacing

Table 5. Input Card Parts List

Z GRID DIGITAL SIGNAL PROCESSOR

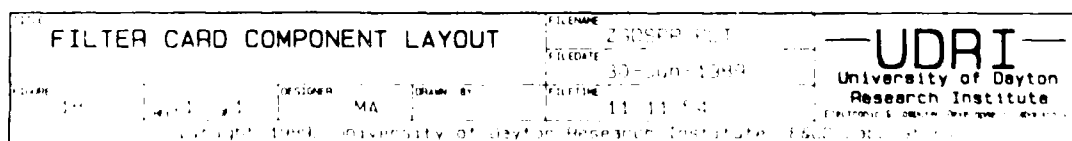
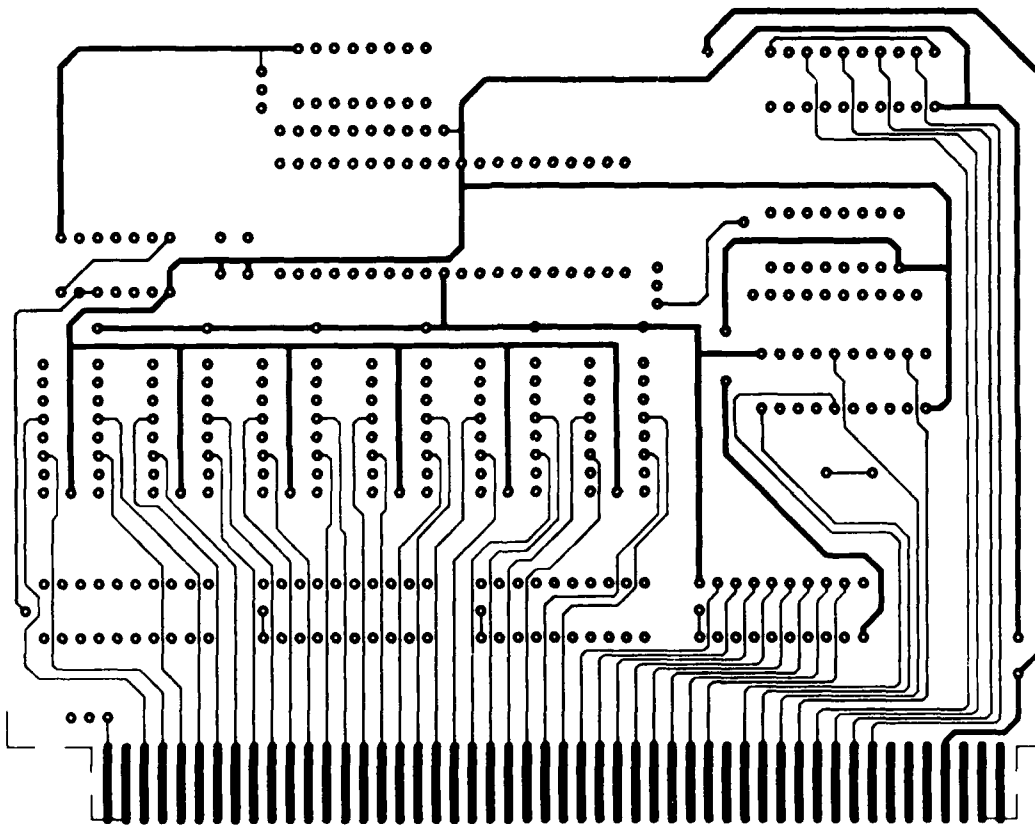
[illegible]

Figure 18. Filter Card Component Layout

COMPONENT



FILTER CARD ARTWORK
(COMPONENT SIDE)

FILENAME: A000001
DATE: 08-12-1993
TIME: 11:18:14

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Research Institute

Figure 19. Filter Card Artwork (Component Side)

FILTER CARD ARTWORK (SOLDER SIDE)
 DATE: 11/11/93
 DESIGNED BY: [blank]
 DRAWN BY: [blank]
 CHECKED BY: [blank]
 APPROVED BY: [blank]
 11/11/93
 UDRI
 University of Dayton
 Research Institute
 11/11/93

29

FILTER CARD SCHEMATIC DIAGRAM

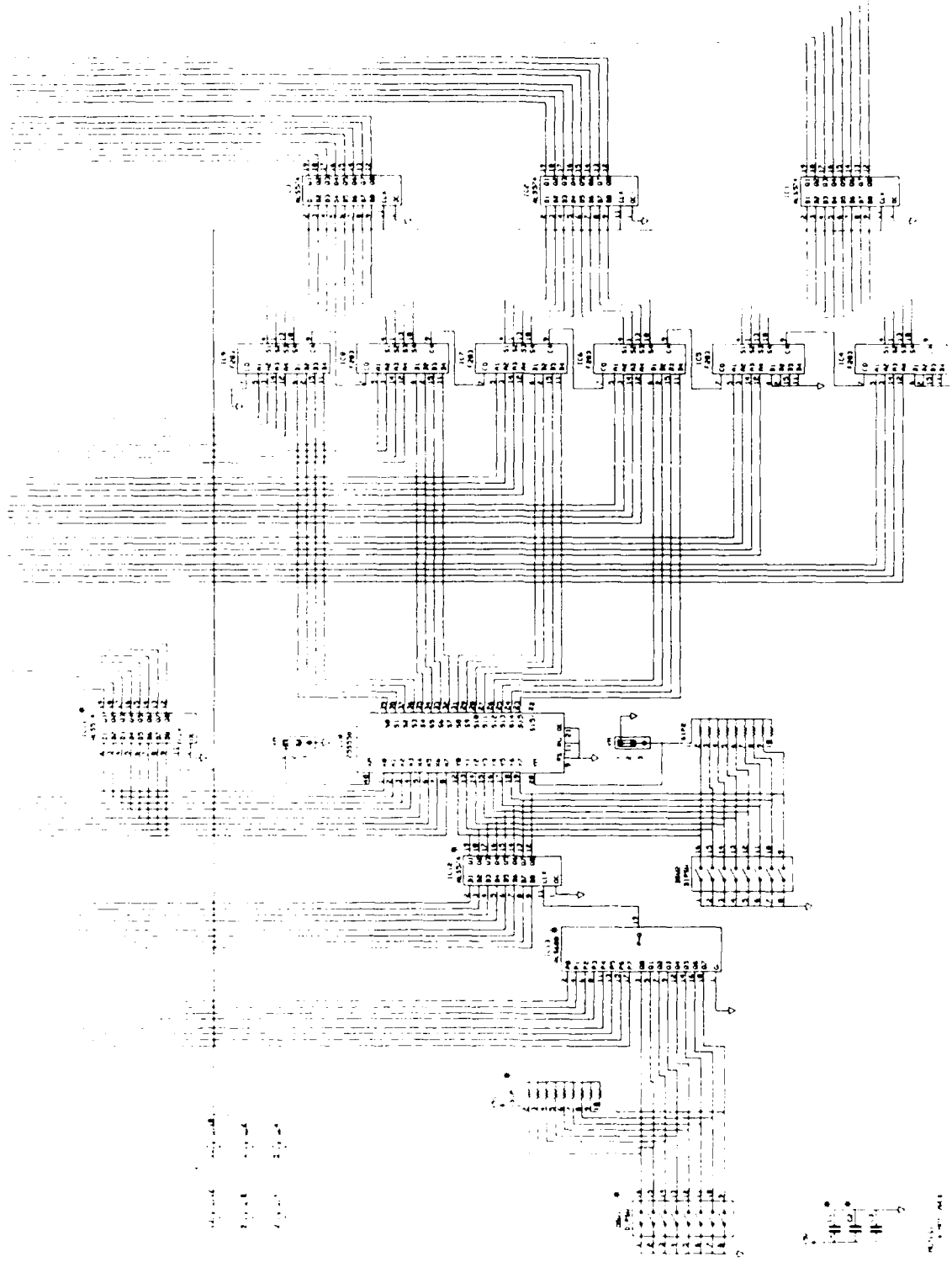


Figure 21. Filter Card Schematic Diagram
30

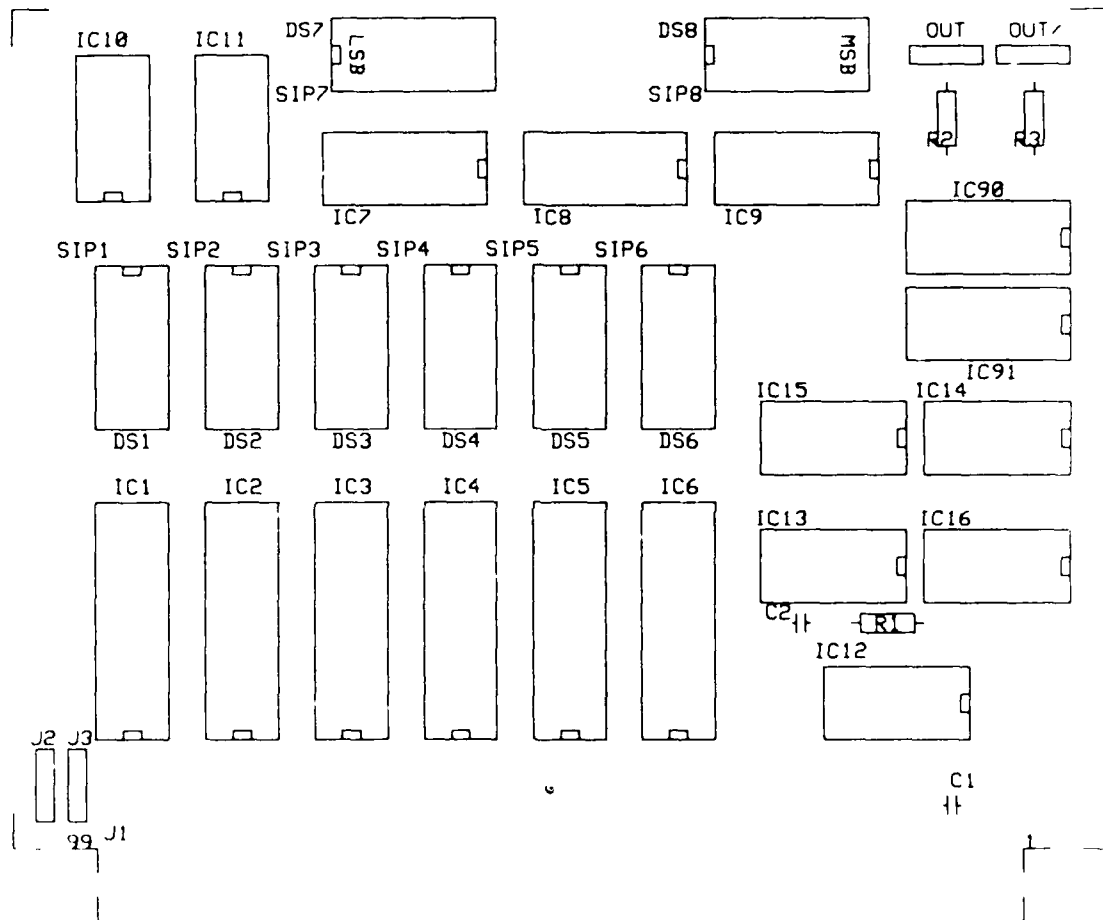
Z-GRID FILTER CARD PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
SIP1	not used	
SIP2	9 resistor SIP	Bourns, 4310R-101-222
C1	not used	
C2	not used	
C3	monocap	0.1 uF
IC1	octal 3-state D flip-flop	74ALS574 (National)
IC2	octal 3-state D flip-flop	74ALS574 (National)
IC3	octal 3-state D flip-flop	74ALS574 (National)
IC4	4-bit binary full adder	74F283 (Fairchild)
IC5	4-bit binary full adder	74F283 (Fairchild)
IC6	4-bit binary full adder	74F283 (Fairchild)
IC7	4-bit binary full adder	74F283 (Fairchild)
IC8	4-bit binary full adder	74F283 (Fairchild)
IC9	4-bit binary full adder	74F283 (Fairchild)
IC10	8-bit x 8-bit multiplier	Am25S558 (AMD)
IC11	not used	
IC12	not used	
IC13	not used	
IC14	hex Schmitt trig inverter	74LS14 (Fairchild)
DSW1	not used	
DSW2	8 channel DIP switch	Grayhill, 76S808
CLK	single row PCB header	3 pin, .1" spacing
XM	single row PCB header	3 pin, .1" spacing
YM	single row PCB header	3 pin, .1" spacing

Table 6. Filter Card Parts List

Z GRID DIGITAL SIGNAL PROCESSOR

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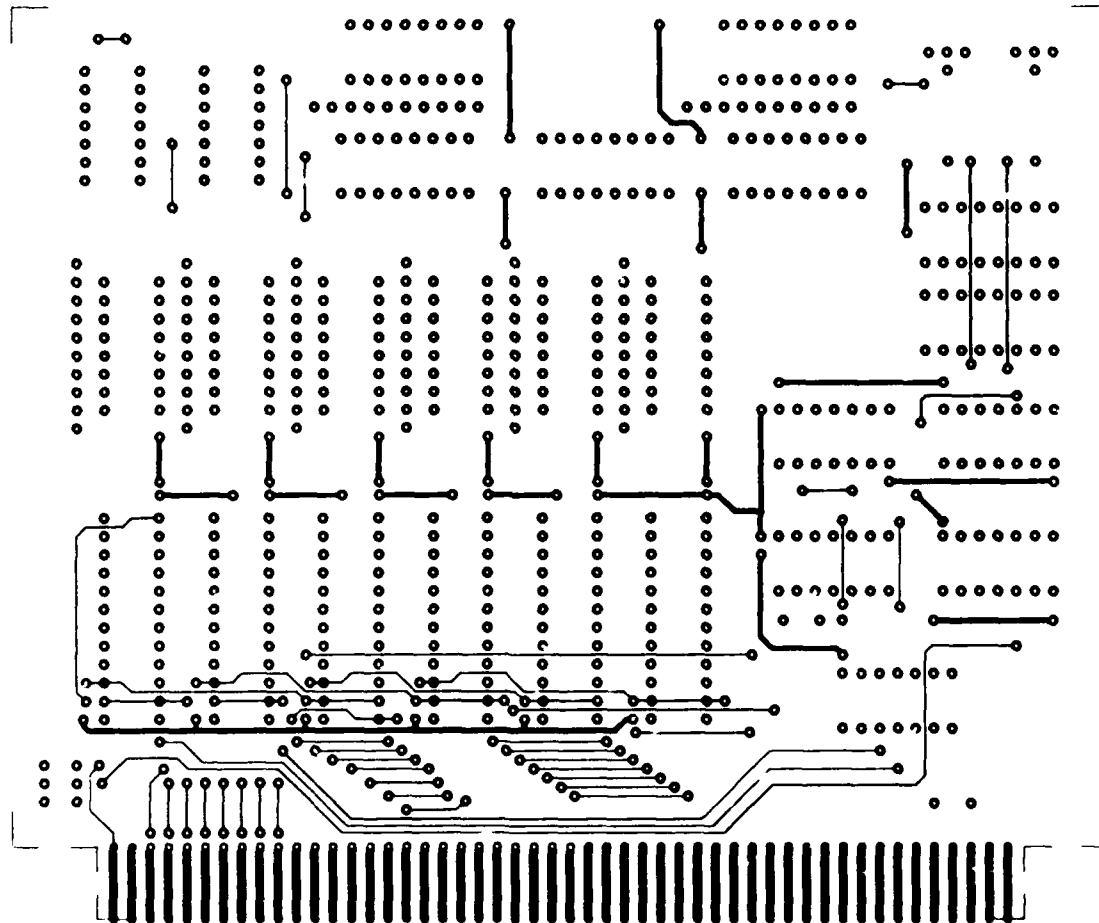
OUTPUT CARD COMPONENT LAYOUT

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Figure 22. Output Card Component Layout

COMPONENT

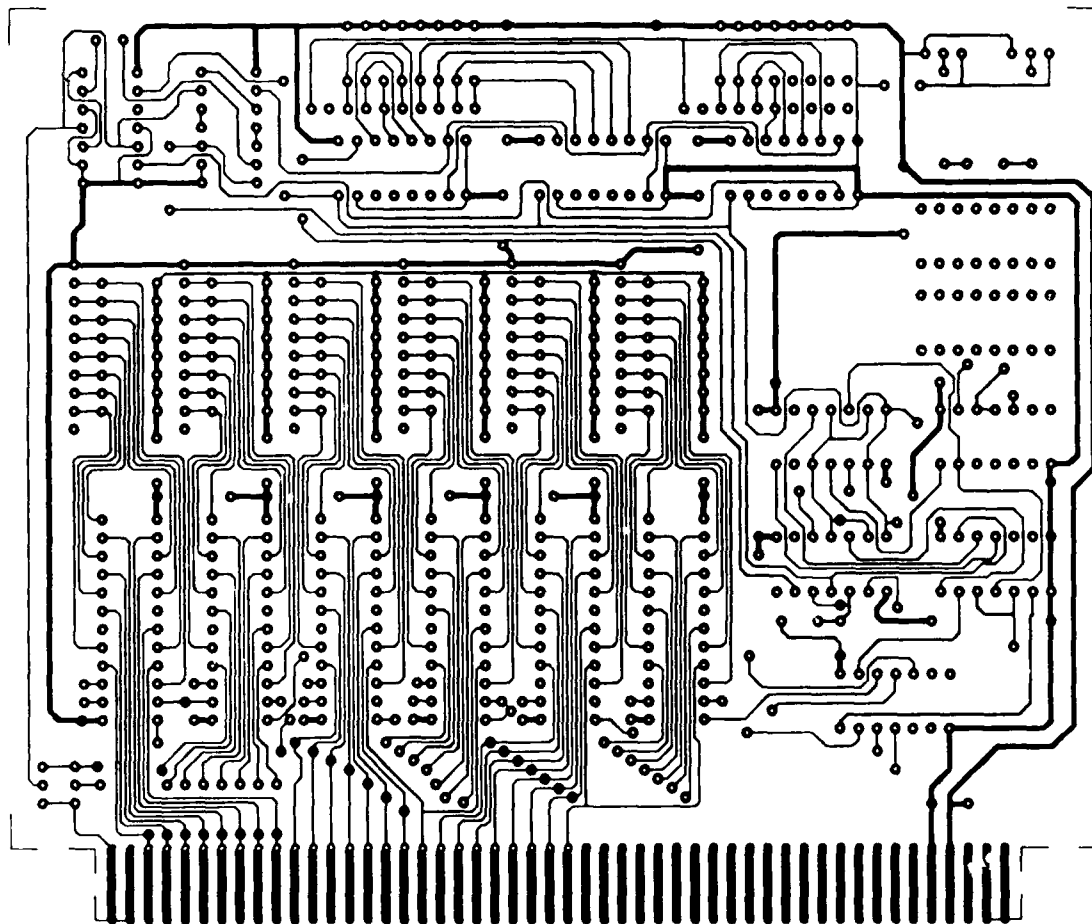


OUTPUT CARD ARTWORK
(COMPONENT SIDE)

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Figure 23. Output Card Artwork (Component Side)

SOLDER



OUTPUT CARD ARTWORK (SOLDER
SIDE)

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Figure 24. Output Card Artwork (Solder Side)

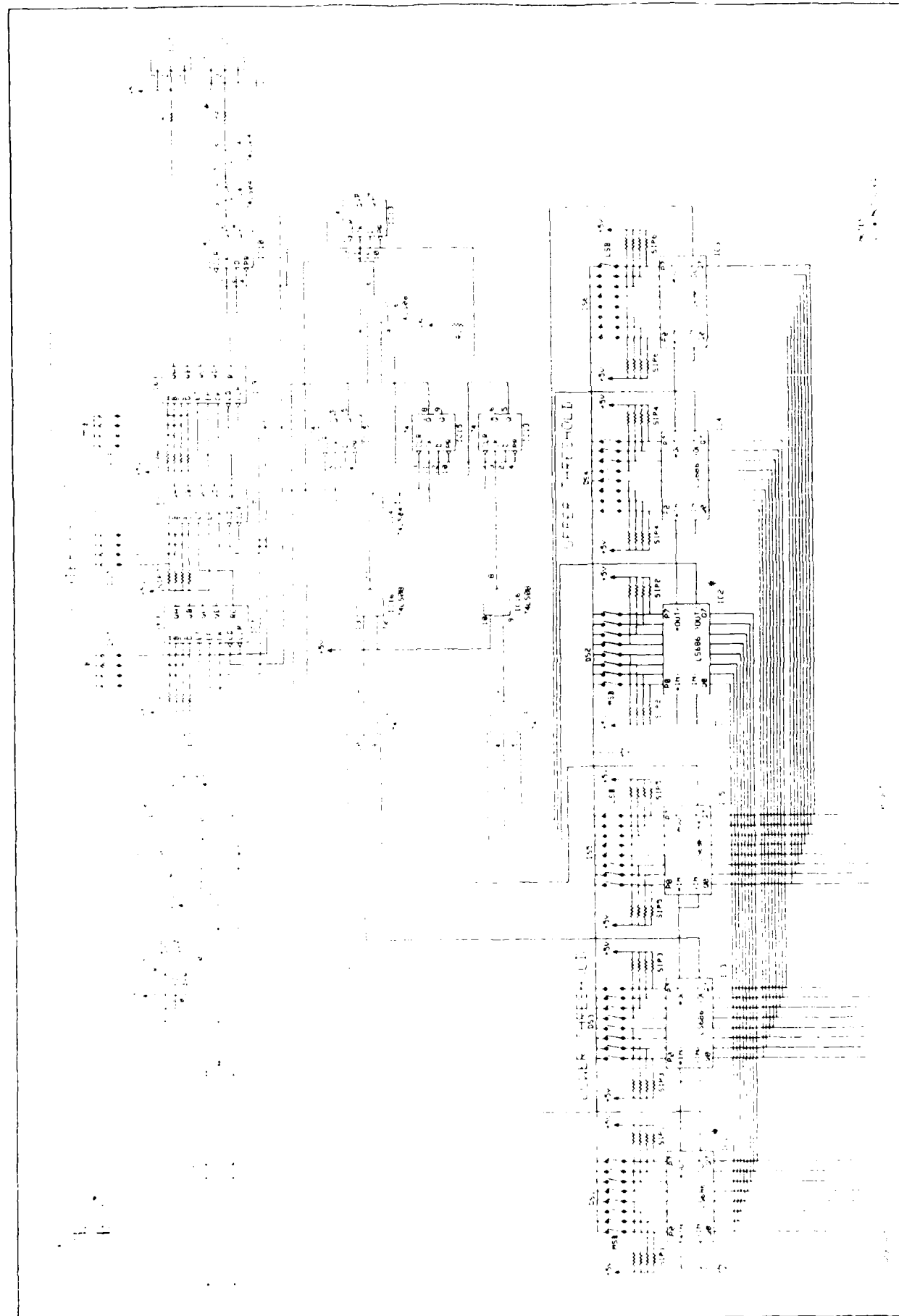


Figure 25. Output Card Schematic Diagram

Z-GRID OUTPUT CARD PARTS LIST

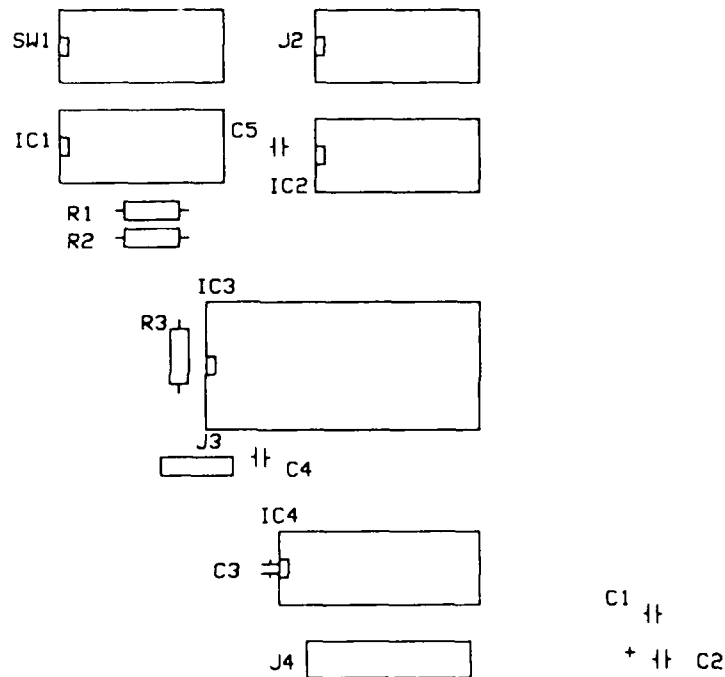
REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	5% tol., 1/4 W resistor	2.2K Ohm
R2	not used	replace with jumper
R3	1% tol., 1/4 W resistor	2.0K Ohm
SIP1	9 resistor SIP	Bourns, 4310R-101-222
SIP2	9 resistor SIP	Bourns, 4310R-101-222
SIP3	9 resistor SIP	Bourns, 4310R-101-222
SIP4	9 resistor SIP	Bourns, 4310R-101-222
SIP5	9 resistor SIP	Bourns, 4310R-101-222
SIP6	9 resistor SIP	Bourns, 4310R-101-222
SIP7	9 resistor SIP	Bourns, 4310R-101-222
SIP8	9 resistor SIP	Bourns, 4310R-101-222
C1	16V tantalum capacitor	10 uF
C2	not used	
IC1	not used	short pins 12, 22
IC2	not used	short pins 12, 22
IC3	8-bit comparator	74LS686 (Motorola)
IC4	8-bit comparator	74LS686 (Motorola)
IC5	8-bit comparator	74LS686 (Motorola)
IC6	8-bit comparator	74LS686 (Motorola)
IC7	4-bit binary counter	74LS163 (National)
IC8	4-bit binary counter	74LS163 (National)
IC9	4-bit binary counter	74LS163 (National)
IC10	dual D flip-flop	74LS74 (National)
IC11	quad 2-input NAND gate	74LS00 (National)
IC12	triple 3-input AND gate	7411 (Signetics)
IC13	dual D flip-flop	74LS74 (National)
IC14	hex inverter	74LS04 (National)
IC15	dual D flip-flop	74LS74 (National)
IC16	quad 2-input AND gate	74LS08 (National)
IC90	not used	
IC91	not used	
DS1	8 channel DIP switch	Grayhill, 76S808
DS2	8 channel DIP switch	Grayhill, 76S808
DS3	8 channel DIP switch	Grayhill, 76S808
DS4	8 channel DIP switch	Grayhill, 76S808
DS5	8 channel DIP switch	Grayhill, 76S808
DS6	8 channel DIP switch	Grayhill, 76S808
DS7	8 channel DIP switch	Grayhill, 76S808
DS8	8 channel DIP switch	Grayhill, 76S808
J2	single row PCB header	3 pin, .1" spacing
J3	single row PCB header	3 pin, .1" spacing
OUT	1 row, rt. angle header	3 pin, .1" spacing
OUT/	1 row, rt. angle header	3 pin, .1" spacing

Table 7. Output Card Parts List

9/29/88

Z GRID DIGITAL SIGNAL PROCESSOR

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99 J1

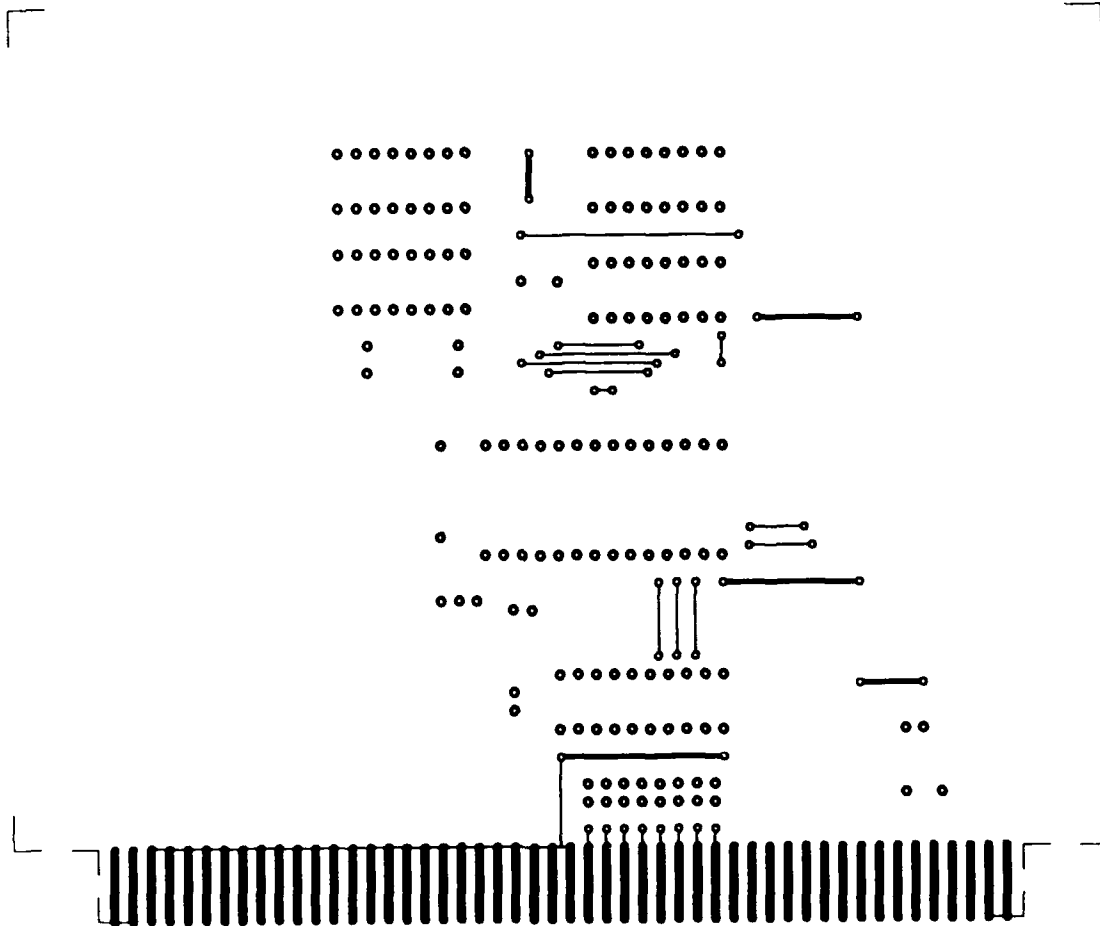
INPUT TEST CARD COMPONENT
LAYOUT

DATE: 9/29/88
TIME: 11:21:50

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Research Institute
ELECTRONIC RESEARCH AND DEVELOPMENT

Figure 26. Input Test Card Component Layout

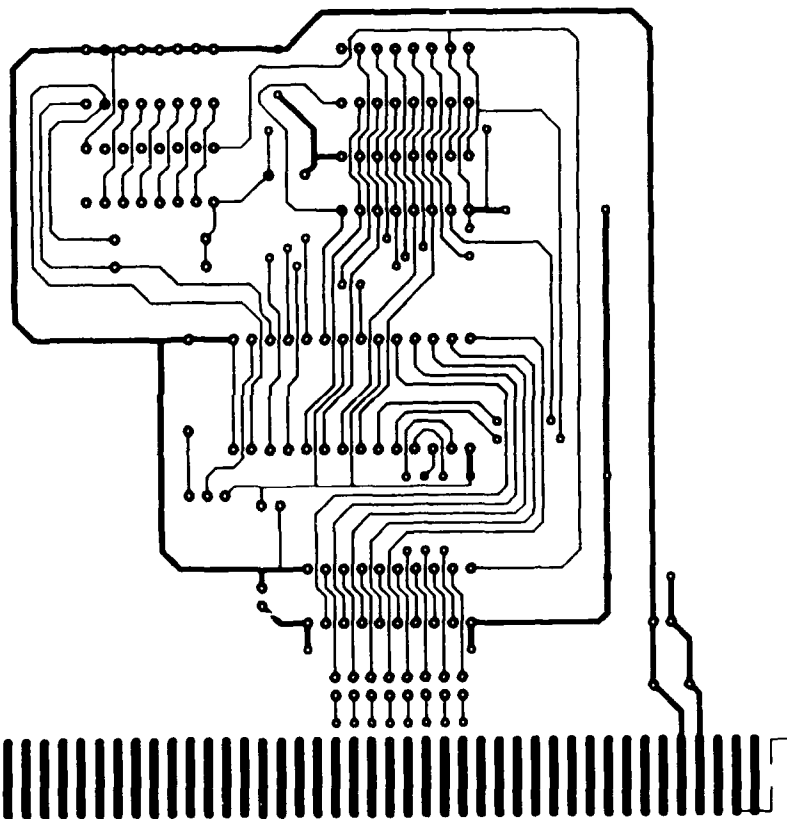
COMPONENT



INPUT TEST CARD ARTWORK (COMPONENT SIDE)		FILENAME PROJECT DATE TIME	UDRI University of Dayton Research Institute
---	--	-------------------------------------	---

Figure 27. Input Test Card Artwork (Component Side)

SOLDER



INPUT TEST CARD ARTWORK (SOLDER SIDE)		FILENAME INPUTSTA.PLT	UDRI University of Dayton Research Institute <small>Electronic & Computer Research Institute</small>
DATE 10/10/89	DESIGNED BY MA	FILEDATE 10/10/89	
<small>Copyright 1989, University of Dayton Research Institute ESCD Laboratory</small>			

Figure 28. Input Test Card Artwork (Solder Side)

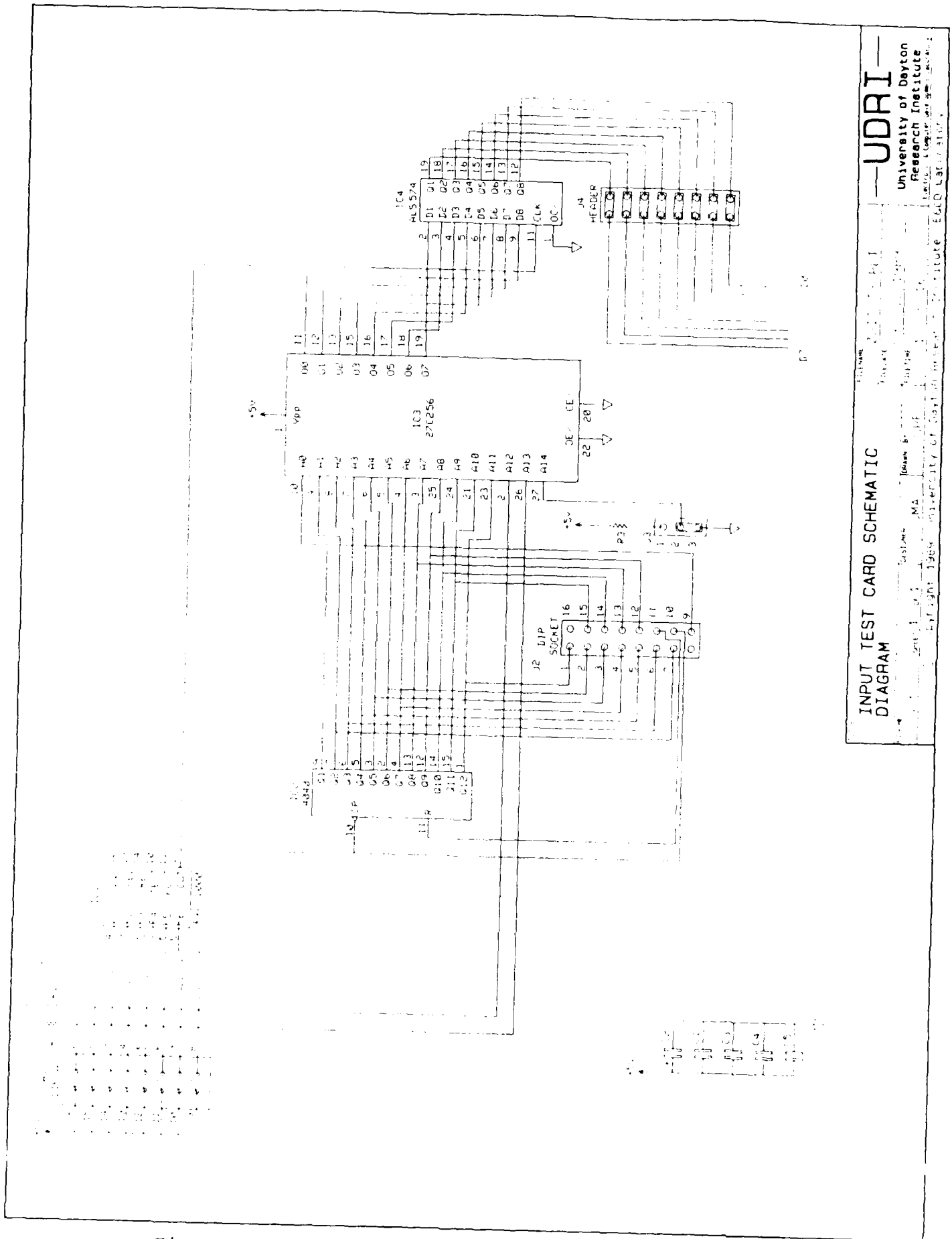


Figure 29. Input Test Card Schematic Diagram

INPUT TEST CARD SCHEMATIC
DIAGRAM

UDRI

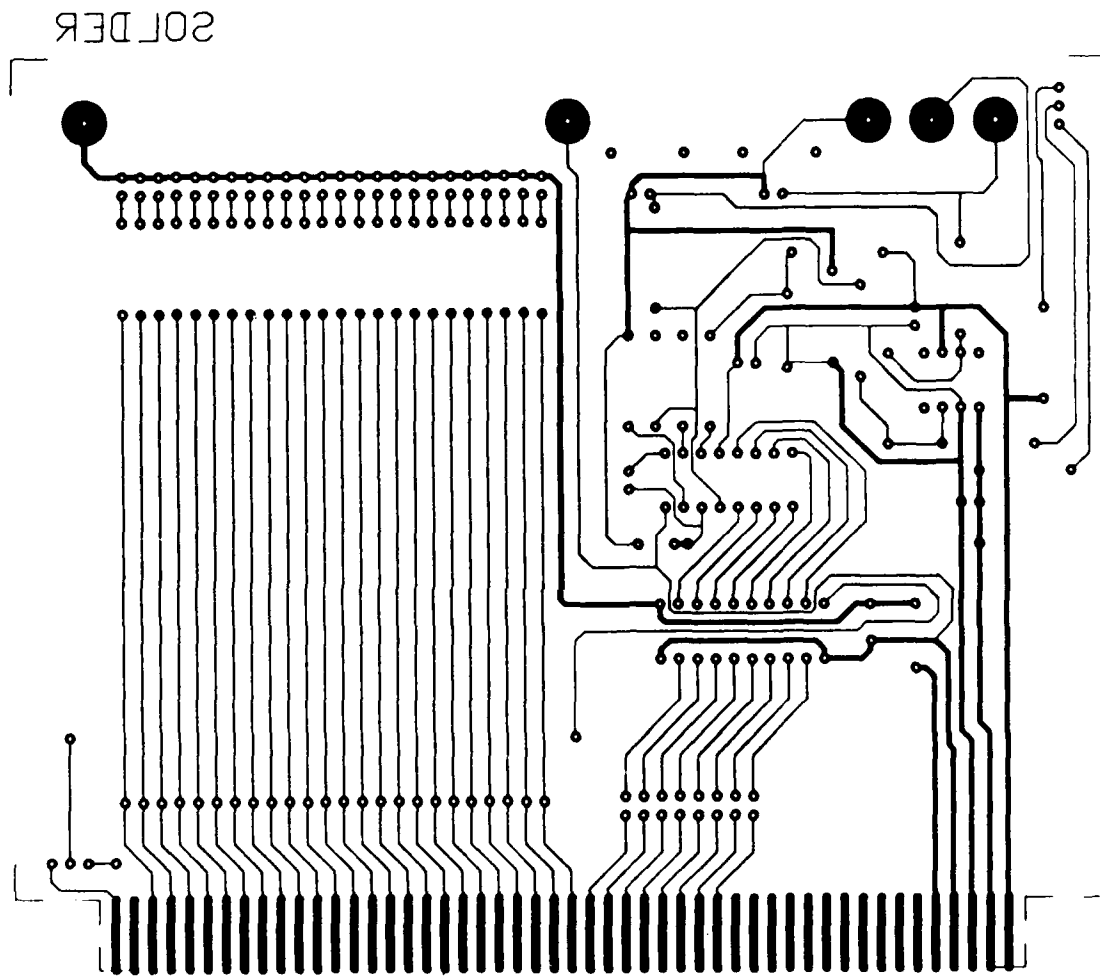
University of Dayton
Research Institute
1000 University of Dayton
Dayton, Ohio 45424-1000

UDRI
University of Dayton
Research Institute
1000 University of Dayton
Dayton, Ohio 45424-1000

Z GRID INPUT TEST CARD PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	5% tol., 1/4 W resistor	2.2K Ohm
R2	5% tol., 1/4 W resistor	2.2K Ohm
R3	5% tol., 1/4 W resistor	2.2K Ohm
C1	monocap	0.1 uF
C2	16V tantalum capacitor	10 uF
C3	monocap	0.1 uF
C4	monocap	0.1 uF
C5	monocap	0.1 uF
IC1	oscillator	PXO-1000 (Statek)
IC2	12-stage binary counter	74HC4040 (National)
IC3	256k (32K x 8) EPROM	27C256 (National)
IC4	octal 3-state D flip-flop	74ALS574 (National)
SW1	8-channel DIP switch	Grayhill, 76SB08
J2	16 pin DIP socket	
J3	single row PCB header	3 pin, .1" spacing
J4	dual row PCB header	16 pin, .1" spacing

Table 3. Input Test Card Parts List



OUTPUT TEST CARD ARTWORK
(SOLDER SIDE)

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Figure 32. Output Test Card Artwork (Solder Side)

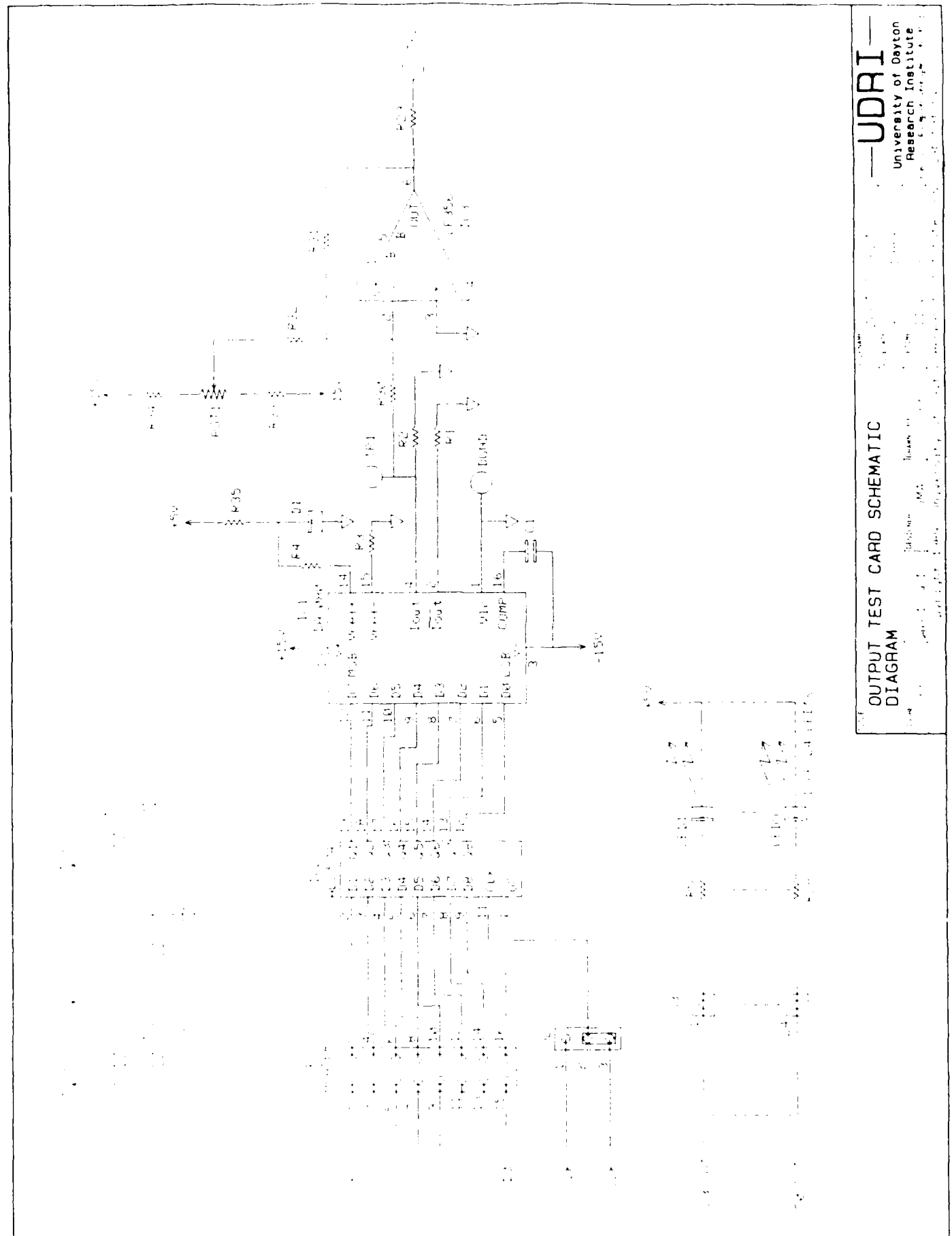


Figure 33. Output Test Card Schematic Diagram

Z-GRID OUTPUT TEST CARD PARTS LIST

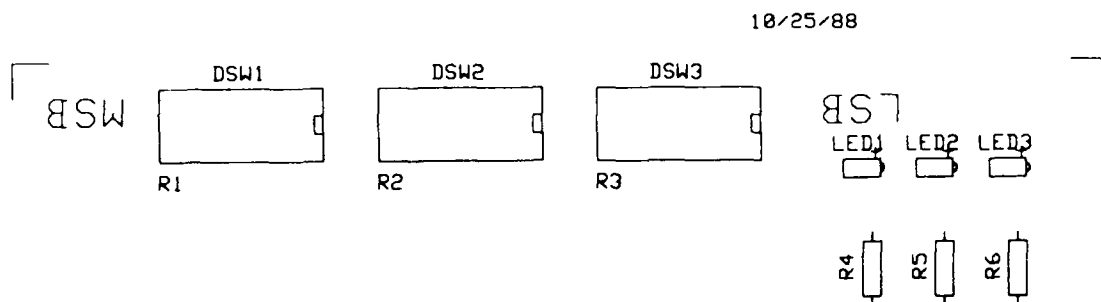
REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	1% tol., 1/4 W resistor	499 Ohm
R2	1% tol., 1/4 W resistor	499 Ohm
R3	1% tol., 1/4 W resistor	3.4K Ohm
R4	1% tol., 1/4 W resistor	3.4K Ohm
R5	5% tol., 1/4 W resistor	680 Ohm
R6	5% tol., 1/4 W resistor	680 Ohm
R7	5% tol., 1/4 W resistor	680 Ohm
R8	5% tol., 1/4 W resistor	680 Ohm
R9	5% tol., 1/4 W resistor	680 Ohm
R10	5% tol., 1/4 W resistor	680 Ohm
R11	5% tol., 1/4 W resistor	680 Ohm
R12	5% tol., 1/4 W resistor	680 Ohm
R13	5% tol., 1/4 W resistor	680 Ohm
R14	5% tol., 1/4 W resistor	680 Ohm
R15	5% tol., 1/4 W resistor	680 Ohm
R16	5% tol., 1/4 W resistor	680 Ohm
R17	5% tol., 1/4 W resistor	680 Ohm
R18	5% tol., 1/4 W resistor	680 Ohm
R19	5% tol., 1/4 W resistor	680 Ohm
R20	5% tol., 1/4 W resistor	680 Ohm
R21	5% tol., 1/4 W resistor	680 Ohm
R22	5% tol., 1/4 W resistor	680 Ohm
R23	5% tol., 1/4 W resistor	680 Ohm
R24	5% tol., 1/4 W resistor	680 Ohm
R25	5% tol., 1/4 W resistor	680 Ohm
R26	5% tol., 1/4 W resistor	680 Ohm
R27	5% tol., 1/4 W resistor	680 Ohm
R28	5% tol., 1/4 W resistor	680 Ohm
R29	1% tol., 1/4 W resistor	100 Ohm
R30	1% tol., 1/4 W resistor	10K Ohm
R31	1% tol., 1/4 W resistor	1K Ohm
R32	1% tol., 1/4 W resistor	20K Ohm
R33	1% tol., 1/4 W resistor	20K Ohm
R34	1% tol., 1/4 W resistor	20K Ohm
R35	1% tol., 1/4 W resistor	887 Ohm
POT1	10-turn PCB trimpot	10K Ohm Bourns 3299
C1	monocap	0.001 uF
C2	monocap	0.1 uF
C3	monocap	0.1 uF
C4	monocap	0.1 uF
C5	monocap	0.1 uF
C6	monocap	0.1 uF
D1	junction diode	1N754
LED1	PCB-mount LED	Dialco, 555-2003
LED2	PCB-mount LED	Dialco, 555-2003
LED3	PCB-mount LED	Dialco, 555-2003
LED4	PCB-mount LED	Dialco, 555-2003
LED5	PCB-mount LED	Dialco, 555-2003
LED6	PCB-mount LED	Dialco, 555-2003
LED7	PCB-mount LED	Dialco, 555-2003
LED8	PCB-mount LED	Dialco, 555-2003
LED9	PCB-mount LED	Dialco, 555-2003
LEDA	PCB-mount LED	Dialco, 555-2003
LEDB	PCB-mount LED	Dialco, 555-2003
LEDC	PCB-mount LED	Dialco, 555-2003
LEDD	PCB-mount LED	Dialco, 555-2003
LEDE	PCB-mount LED	Dialco, 555-2003
LEDF	PCB-mount LED	Dialco, 555-2003
LEDG	PCB-mount LED	Dialco, 555-2003
LEDH	PCB-mount LED	Dialco, 555-2003
LEDI	PCB-mount LED	Dialco, 555-2003
LEDJ	PCB-mount LED	Dialco, 555-2003
LEDK	PCB-mount LED	Dialco, 555-2003
LEDL	PCB-mount LED	Dialco, 555-2003
LEDM	PCB-mount LED	Dialco, 555-2003
LEDN	PCB-mount LED	Dialco, 555-2003
LEDO	PCB-mount LED	Dialco, 555-2003

Table 9a. Output Test Card Parts List (1 of 2)

Z-GRID OUTPUT TEST CARD PARTS LIST

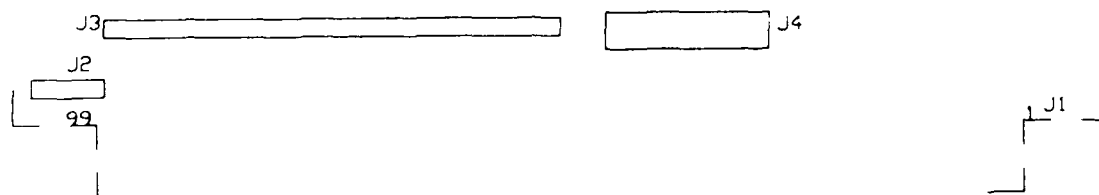
REFERENCE	DESCRIPTION	PART NUMBER/VALUE
IC1	8-bit D/A converter	DAC0800 (National)
IC2	octal 3-state D flip flop	74ALS574 (National)
IC3	JFET operational amp.	LF356 (National)
J2	single row PCB header	3 pin, .1" spacing
J3	single row PCB header	24 pin, .1" spacing
J4	dual row PCB header	16 pin, .1" spacing

Table 9b. Output Test Card Parts List (2 of 2)



Z GRID DIGITAL SIGNAL PROCESSOR

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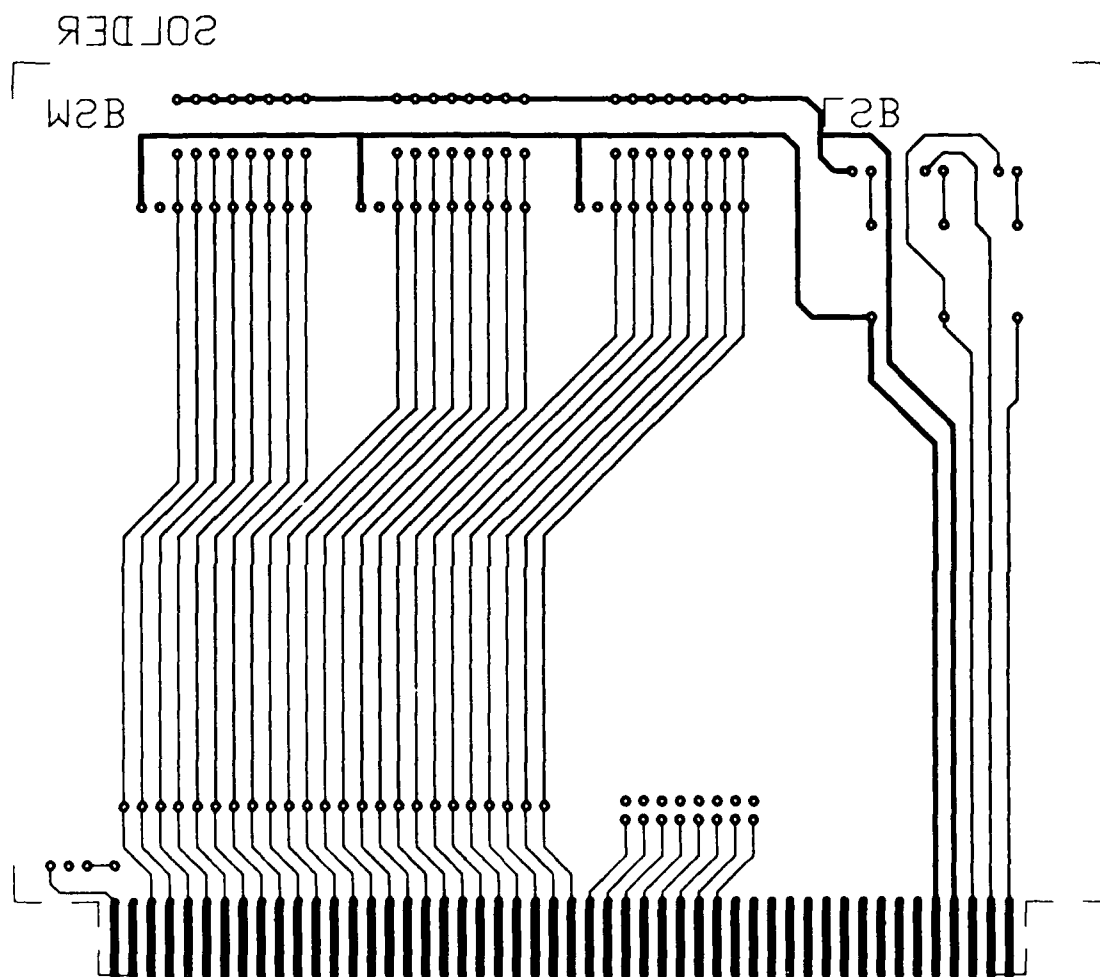


OFFSET CARD COMPONENT LAYOUT

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Figure 34. Offset Card Component Layout



OFFSET CARD ARTWORK

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Figure 35. Offset Card Artwork

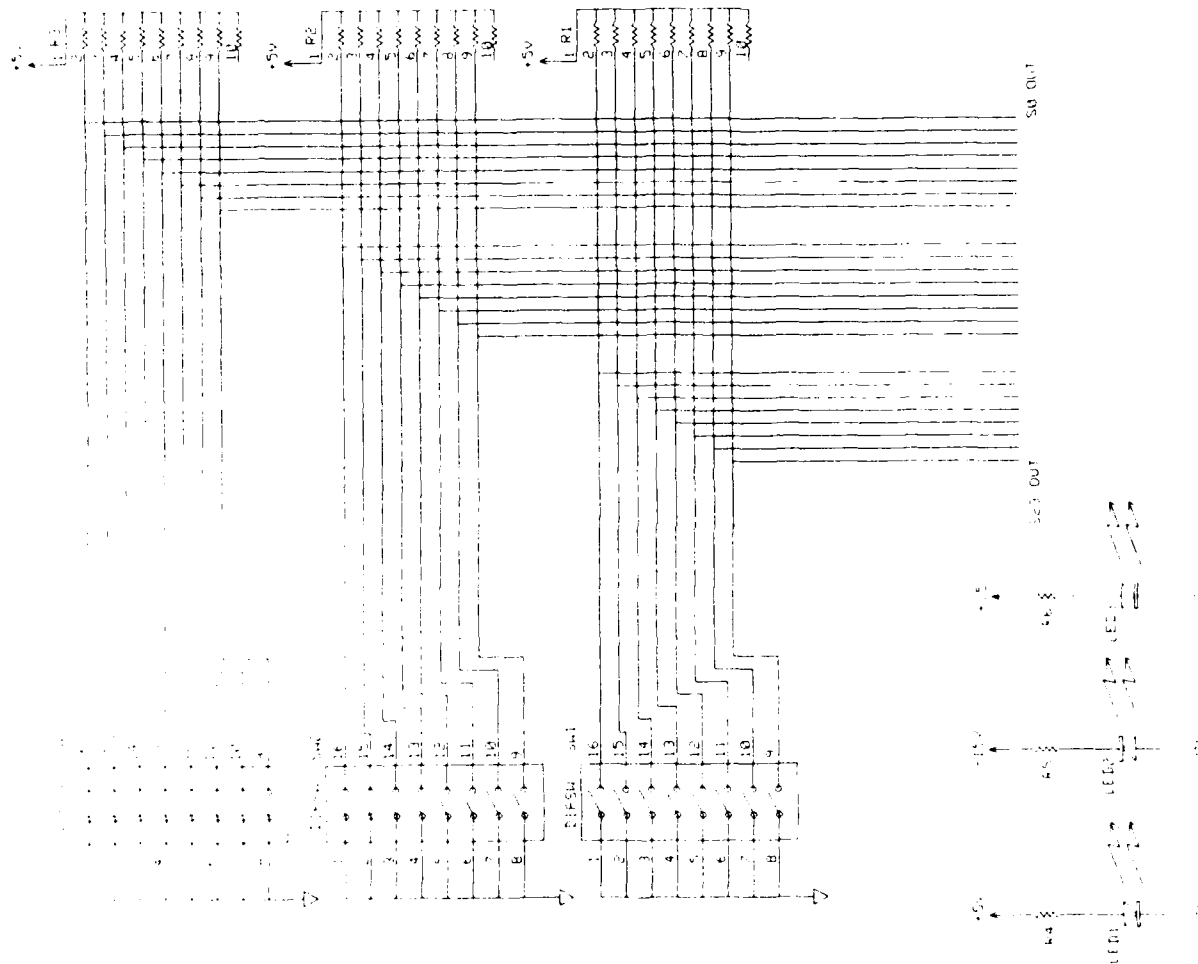


Figure 36. Offset Card Schematic Diagram
50

Z-GRID OFFSET CARD PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
R1	9 resistor SIP	Bourns, 4310R-101-222
R2	9 resistor SIP	Bourns, 4310R-101-222
R3	9 resistor SIP	Bourns, 4310R-101-222
R4	5% tol., 1/4 W resistor	330 Ohm
R5	1% tol., 1/4 W resistor	1.21K Ohm
R6	1% tol., 1/4 W resistor	1.21K Ohm
LED1	jumbo green LED	
LED2	jumbo green LED	
LED3	jumbo green LED	
DSW1	8 channel DIP switch	Grayhill, 76S808
DSW2	8 channel DIP switch	Grayhill, 76S808
DSW3	8 channel DIP switch	Grayhill, 76S808
J2	not used	
J3	not used	
J4	not used	

Table 10. Offset Card Parts List

9/17/88

Z-GRID BACKPLANE

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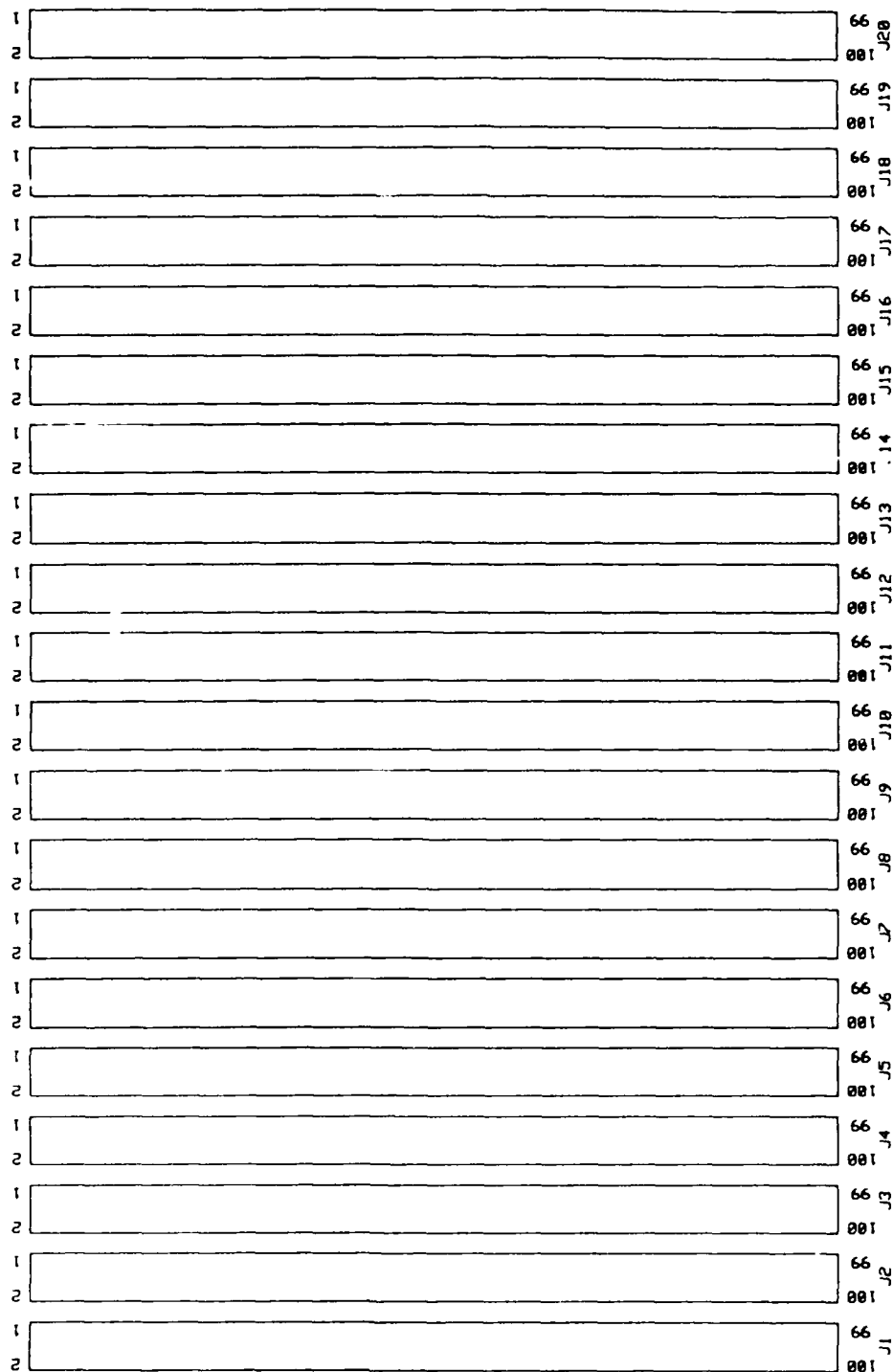
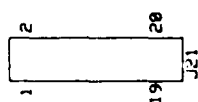
V+

V-

AGND

DGND

VCC



SYSTEM BACKPLANE COMPONENT LAYOUT

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Figure 37. System Backplane Component Layout

Z-GRID BACKPLANE PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER/VALUE
J1	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J2	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J3	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J4	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J5	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J6	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J7	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J8	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J9	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J10	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J11	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J12	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J13	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J14	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J15	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J16	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J17	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J18	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J19	100 pin, 2 row connector	TRW Cinch, 50-100C-20
J20	100 pin, 2 row connector	TRW Cinch, 50-100C-20

Table 11. System Backplane Parts List

Z GRID BACKPLANE

11/28/88

INPUT	INPUT TEST	DSP	OUTPUT	OUTPUT TEST	OSC	PIN #	INPUT	INPUT TEST	DSP	OUTPUT	OUTPUT TEST	OSC
+15V	+15V	+15V	+15V	+15V	+15V	1 2	+15V	+15V	+15V	+15V	+15V	+15V
-15V	-15V	-15V	-15V	-15V	-15V	3 4	-15V	-15V	-15V	-15V	-15V	-15V
A GND	A GND	A GND	A GND	A GND	A GND	5 6	A GND	A GND	A GND	A GND	A GND	A GND
D GND	D GND	D GND	D GND	D GND	D GND	7 8	D GND	D GND	D GND	D GND	D GND	D GND
+5V	+5V	+5V	+5V	+5V	+5V	9 10	+5V	+5V	+5V	+5V	+5V	+5V
						11 12						
						13 14						
		C0				15 16			C1			
		C2				17 18			C3			
		C4				19 20			C5			
		C6				21 22			C7			
		A0				23 24			A1			
		A2				25 26			A3			
		A4				27 28			A5			
		A6				29 30			A7			
D GND	D GND	D GND	D GND	D GND	D GND	31 32	D GND	D GND	D GND	D GND	D GND	D GND
		D0	D0	D0		33 34	D0	D0	D0	D0	D0	
		D1	D1	D1		35 36	D1	D1	D1	D1	D1	
		D2	D2	D2		37 38	D2	D2	D2	D2	D2	
		D3	D3	D3		39 40	D3	D3	D3	D3	D3	
		D4	D4	D4		41 42	D4	D4	D4	D4	D4	
		D5	D5	D5		43 44	D5	D5	D5	D5	D5	
		D6	D6	D6		45 46	D6	D6	D6	D6	D6	
		D7	D7	D7		47 48	D7	D7	D7	D7	D7	
		S0 IN	S0 IN	S0 IN		49 50	S0 OUT	S0 OUT	S0 OUT	S0 OUT	S0 OUT	
		S1 IN	S1 IN	S1 IN		51 52	S1 OUT	S1 OUT	S1 OUT	S1 OUT	S1 OUT	
		S2 IN	S2 IN	S2 IN		53 54	S2 OUT	S2 OUT	S2 OUT	S2 OUT	S2 OUT	
		S3 IN	S3 IN	S3 IN		55 56	S3 OUT	S3 OUT	S3 OUT	S3 OUT	S3 OUT	
		S4 IN	S4 IN	S4 IN		57 58	S4 OUT	S4 OUT	S4 OUT	S4 OUT	S4 OUT	
		S5 IN	S5 IN	S5 IN		59 60	S5 OUT	S5 OUT	S5 OUT	S5 OUT	S5 OUT	
		S6 IN	S6 IN	S6 IN		61 62	S6 OUT	S6 OUT	S6 OUT	S6 OUT	S6 OUT	
		S7 IN	S7 IN	S7 IN		63 64	S7 OUT	S7 OUT	S7 OUT	S7 OUT	S7 OUT	
		S8 IN	S8 IN	S8 IN		65 66	S8 OUT	S8 OUT	S8 OUT	S8 OUT	S8 OUT	
		S9 IN	S9 IN	S9 IN		67 68	S9 OUT	S9 OUT	S9 OUT	S9 OUT	S9 OUT	
		S10 IN	S10 IN	S10 IN		69 70	S10 OUT	S10 OUT	S10 OUT	S10 OUT	S10 OUT	
		S11 IN	S11 IN	S11 IN		71 72	S11 OUT	S11 OUT	S11 OUT	S11 OUT	S11 OUT	
		S12 IN	S12 IN	S12 IN		73 74	S12 OUT	S12 OUT	S12 OUT	S12 OUT	S12 OUT	
		S13 IN	S13 IN	S13 IN		75 76	S13 OUT	S13 OUT	S13 OUT	S13 OUT	S13 OUT	
		S14 IN	S14 IN	S14 IN		77 78	S14 OUT	S14 OUT	S14 OUT	S14 OUT	S14 OUT	
		S15 IN	S15 IN	S15 IN		79 80	S15 OUT	S15 OUT	S15 OUT	S15 OUT	S15 OUT	
		S16 IN	S16 IN	S16 IN		81 82	S16 OUT	S16 OUT	S16 OUT	S16 OUT	S16 OUT	
		S17 IN	S17 IN	S17 IN		83 84	S17 OUT	S17 OUT	S17 OUT	S17 OUT	S17 OUT	
		S18 IN	S18 IN	S18 IN		85 86	S18 OUT	S18 OUT	S18 OUT	S18 OUT	S18 OUT	
		S19 IN	S19 IN	S19 IN		87 88	S19 OUT	S19 OUT	S19 OUT	S19 OUT	S19 OUT	
		S20 IN	S20 IN	S20 IN		89 90	S20 OUT	S20 OUT	S20 OUT	S20 OUT	S20 OUT	
		S21 IN	S21 IN	S21 IN		91 92	S21 OUT	S21 OUT	S21 OUT	S21 OUT	S21 OUT	
		S22 IN	S22 IN	S22 IN		93 94	S22 OUT	S22 OUT	S22 OUT	S22 OUT	S22 OUT	
		S23 IN	S23 IN	S23 IN		95 96	S23 OUT	S23 OUT	S23 OUT	S23 OUT	S23 OUT	
D GND	D GND	D GND	D GND	D GND	D GND	97 98	D GND	D GND	D GND	D GND	D GND	D GND
CLOCK	CLOCK	CLOCK	CLOCK	CLOCK	CLOCK	99 100	CLOCK/	CLOCK/	CLOCK/	CLOCK/	CLOCK/	CLOCK/

Table 12. System Backplane Pinout

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5. INPUT CARD EPROM LISTING

Note. All listings begin at EPROM address 0000, which corresponds to the first listing address 1A00

1 1A00.1AFF

```

00/1A00:80 7F 7E 7D 7C 7B 7A 79-..~)|{zy
00/1A08:78 77 76 75 74 73 72 71-xwvutsrq
00/1A10:70 6F 6E 6D 6C 6B 6A 69-ponmlkji
00/1A18:68 67 66 65 64 63 62 61-hgfedcba
00/1A20:60 5F 5E 5D 5C 5B 5A 59-`_^]\[ZY
00/1A28:58 57 56 55 54 53 52 51-XWVUTSRQ
00/1A30:50 4F 4E 4D 4C 4B 4A 49-PONMLKJI
00/1A38:48 47 46 45 44 43 42 41-HGFEDCBA
00/1A40:40 3F 3E 3D 3C 3B 3A 39-@?>=<;:9
00/1A48:38 37 36 35 34 33 32 31-87654321
00/1A50:30 2F 2E 2D 2C 2B 2A 29-0/.-,+*)
00/1A58:28 27 26 25 24 23 22 21-('&$%#"!
00/1A60:20 1F 1E 1D 1C 1B 1A 19- .....
00/1A68:18 17 16 15 14 13 12 11- .....
00/1A70:10 0F 0E 0D 0C 0B 0A 09- .....
00/1A78:08 07 06 05 04 03 02 01- .....
00/1A80:00 01 02 03 04 05 06 07- .....
00/1A88:08 09 0A 0B 0C 0D 0E 0F- .....
00/1A90:10 11 12 13 14 15 16 17- .....
00/1A98:18 19 1A 1B 1C 1D 1E 1F- .....
00/1AA0:20 21 22 23 24 25 26 27- !"#$%&'
00/1AA8:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
00/1AB0:30 31 32 33 34 35 36 37-01234567
00/1AB8:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
00/1AC0:40 41 42 43 44 45 46 47-@ABCDEFGH
00/1AC8:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
00/1AD0:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
00/1AD8:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/1AE0:60 61 62 63 64 65 66 67-`abcdefg
00/1AE8:68 69 6A 6B 6C 6D 6E 6F-hijklmno
00/1AF0:70 71 72 73 74 75 76 77-pqrstuvw
00/1AF8:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.

```

*

6. INPUT TEST CARD EPROM LISTING

Note. All listings begin at EPROM address 0000, which corresponds to the first listing address 1A00.

[illegible]

[illegible]

[illegible]

64

```

00/2350:10 10 10 10 10 10 10 10-.....
00/2358:10 10 10 10 10 10 10 10-.....
00/2360:10 10 10 10 10 10 10 10-.....
00/2368:10 10 10 10 10 10 10 10-.....
00/2370:10 10 10 10 10 10 10 10-.....
00/2378:10 10 10 10 10 10 10 10-.....
00/2380:10 10 10 10 10 10 10 10-.....
00/2388:10 10 10 10 10 10 10 10-.....
00/2390:10 10 10 10 10 10 10 10-.....
00/2398:10 10 10 10 10 10 10 10-.....
00/23A0:10 10 10 10 10 10 10 10-.....
00/23A8:10 10 10 10 10 10 10 10-.....
00/23B0:10 10 10 10 10 10 10 10-.....
00/23B8:10 10 10 10 10 10 10 10-.....
00/23C0:10 10 10 10 10 10 10 10-.....
00/23C8:10 10 10 10 10 10 10 10-.....
00/23D0:10 10 10 10 10 10 10 10-.....
00/23D8:10 10 10 10 10 10 10 10-.....
00/23E0:10 10 10 10 10 10 10 10-.....
00/23E8:10 10 10 10 10 10 10 10-.....
00/23F0:10 10 10 10 10 10 10 10-.....
00/23F8:10 10 10 10 10 10 10 10-.....
00/2400:20 20 20 20 20 20 20 20-
00/2408:20 20 20 20 20 20 20 20-
00/2410:20 20 20 20 20 20 20 20-
00/2418:20 20 20 20 20 20 20 20-
00/2420:20 20 20 20 20 20 20 20-
00/2428:20 20 20 20 20 20 20 20-
00/2430:20 20 20 20 20 20 20 20-
00/2438:20 20 20 20 20 20 20 20-
00/2440:20 20 20 20 20 20 20 20-
00/2448:20 20 20 20 20 20 20 20-
00/2450:20 20 20 20 20 20 20 20-
00/2458:20 20 20 20 20 20 20 20-
00/2460:20 20 20 20 20 20 20 20-
00/2468:20 20 20 20 20 20 20 20-
00/2470:20 20 20 20 20 20 20 20-
00/2478:20 20 20 20 20 20 20 20-
00/2480:20 20 20 20 20 20 20 20-
00/2488:20 20 20 20 20 20 20 20-
00/2490:20 20 20 20 20 20 20 20-
00/2498:20 20 20 20 20 20 20 20-
00/24A0:20 20 20 20 20 20 20 20-
00/24A8:20 20 20 20 20 20 20 20-
00/24B0:20 20 20 20 20 20 20 20-
00/24B8:20 20 20 20 20 20 20 20-
00/24C0:20 20 20 20 20 20 20 20-
00/24C8:20 20 20 20 20 20 20 20-
00/24D0:20 20 20 20 20 20 20 20-
00/24D8:20 20 20 20 20 20 20 20-
00/24E0:20 20 20 20 20 20 20 20-
00/24E8:20 20 20 20 20 20 20 20-
00/24F0:20 20 20 20 20 20 20 20-
00/24F8:20 20 20 20 20 20 20 20-
00/2500:20 20 20 20 20 20 20 20-
00/2508:20 20 20 20 20 20 20 20-
00/2510:20 20 20 20 20 20 20 20-
00/2518:20 20 20 20 20 20 20 20-
00/2520:20 20 20 20 20 20 20 20-
00/2528:20 20 20 20 20 20 20 20-

```


[illegible]

[illegible]

```

00/28F0:80 80 80 80 80 80 80 80-.....
00/28F8:80 80 80 80 80 80 80 80-.....
00/2900:80 80 80 80 80 80 80 80-.....
00/2908:80 80 80 80 80 80 80 80-.....
00/2910:80 80 80 80 80 80 80 80-.....
00/2918:80 80 80 80 80 80 80 80-.....
00/2920:80 80 80 80 80 80 80 80-.....
00/2928:80 80 80 80 80 80 80 80-.....
00/2930:80 80 80 80 80 80 80 80-.....
00/2938:80 80 80 80 80 80 80 80-.....
00/2940:80 80 80 80 80 80 80 80-.....
00/2948:80 80 80 80 80 80 80 80-.....
00/2950:80 80 80 80 80 80 80 80-.....
00/2958:80 80 80 80 80 80 80 80-.....
00/2960:80 80 80 80 80 80 80 80-.....
00/2968:80 80 80 80 80 80 80 80-.....
00/2970:80 80 80 80 80 80 80 80-.....
00/2978:80 80 80 80 80 80 80 80-.....
00/2980:80 80 80 80 80 80 80 80-.....
00/2988:80 80 80 80 80 80 80 80-.....
00/2990:80 80 80 80 80 80 80 80-.....
00/2998:80 80 80 80 80 80 80 80-.....
00/29A0:80 80 80 80 80 80 80 80-.....
00/29A8:80 80 80 80 80 80 80 80-.....
00/29B0:80 80 80 80 80 80 80 80-.....
00/29B8:80 80 80 80 80 80 80 80-.....
00/29C0:80 80 80 80 80 80 80 80-.....
00/29C8:80 80 80 80 80 80 80 80-.....
00/29D0:80 80 80 80 80 80 80 80-.....
00/29D8:80 80 80 80 80 80 80 80-.....
00/29E0:80 80 80 80 80 80 80 80-.....
00/29E8:80 80 80 80 80 80 80 80-.....
00/29F0:80 80 80 80 80 80 80 80-.....
00/29F8:80 80 80 80 80 80 80 80-.....
00/2A00:00 01 02 03 04 05 06 07-.....
00/2A08:08 09 0A 0B 0C 0D 0E 0F-.....
00/2A10:10 11 12 13 14 15 16 17-.....
00/2A18:18 19 1A 1B 1C 1D 1E 1F-.....
00/2A20:20 21 22 23 24 25 26 27- !"#$%&'
00/2A28:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
00/2A30:30 31 32 33 34 35 36 37-01234567
00/2A38:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
00/2A40:40 41 42 43 44 45 46 47-@ABCDEFGH
00/2A48:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
00/2A50:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
00/2A58:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/2A60:60 61 62 63 64 65 66 67-`abcdefg
00/2A68:68 69 6A 6B 6C 6D 6E 6F-hijklmno
00/2A70:70 71 72 73 74 75 76 77-pqrstuvw
00/2A78:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
00/2A80:80 81 82 83 84 85 86 87-.....
00/2A88:88 89 8A 8B 8C 8D 8E 8F-.....
00/2A90:90 91 92 93 94 95 96 97-.....
00/2A98:98 99 9A 9B 9C 9D 9E 9F-.....
00/2AA0:A0 A1 A2 A3 A4 A5 A6 A7- !"#$%&'
00/2AA8:A8 A9 AA AB AC AD AE AF-()*+,-./
00/2AB0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
00/2AB8:B8 B9 BA BB BC BD BE BF-89:;<=>?
00/2AC0:C0 C1 C2 C3 C4 C5 C6 C7-@ABCDEFGH
00/2AC8:C8 C9 CA CB CC CD CE CF-HIJKLMNO

```

00/2AD0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/2AD8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/2AE0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
 00/2AE8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/2AF0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
 00/2AF8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/2B00:00 01 02 03 04 05 06 07-.....
 00/2B08:08 09 0A 0B 0C 0D 0E 0F-.....
 00/2B10:10 11 12 13 14 15 16 17-.....
 00/2B18:18 19 1A 1B 1C 1D 1E 1F-.....
 00/2B20:20 21 22 23 24 25 26 27- !"#\$%&'
 00/2B28:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
 00/2B30:30 31 32 33 34 35 36 37-01234567
 00/2B38:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/2B40:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/2B48:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
 00/2B50:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
 00/2B58:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/2B60:60 61 62 63 64 65 66 67-'abcdefgh
 00/2B68:68 69 6A 6B 6C 6D 6E 6F-hijklmno
 00/2B70:70 71 72 73 74 75 76 77-pqrstuvw
 00/2B78:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/2B80:80 81 82 83 84 85 86 87-.....
 00/2B88:88 89 8A 8B 8C 8D 8E 8F-.....
 00/2B90:90 91 92 93 94 95 96 97-.....
 00/2B98:98 99 9A 9B 9C 9D 9E 9F-.....
 00/2BA0:A0 A1 A2 A3 A4 A5 A6 A7- !"#\$%&'
 00/2BA8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/2BB0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/2BB8:B8 B9 BA BB BC BD BE BF-89:;<=>?
 00/2BC0:C0 C1 C2 C3 C4 C5 C6 C7-@ABCDEFGH
 00/2BC8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
 00/2BD0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/2BD8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/2BE0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
 00/2BE8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/2BF0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
 00/2BF8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/2C00:00 01 02 03 04 05 06 07-.....
 00/2C08:08 09 0A 0B 0C 0D 0E 0F-.....
 00/2C10:10 11 12 13 14 15 16 17-.....
 00/2C18:18 19 1A 1B 1C 1D 1E 1F-.....
 00/2C20:20 21 22 23 24 25 26 27- !"#\$%&'
 00/2C28:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
 00/2C30:30 31 32 33 34 35 36 37-01234567
 00/2C38:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/2C40:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/2C48:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
 00/2C50:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
 00/2C58:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/2C60:60 61 62 63 64 65 66 67-'abcdefgh
 00/2C68:68 69 6A 6B 6C 6D 6E 6F-hijklmno
 00/2C70:70 71 72 73 74 75 76 77-pqrstuvw
 00/2C78:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/2C80:80 81 82 83 84 85 86 87-.....
 00/2C88:88 89 8A 8B 8C 8D 8E 8F-.....
 00/2C90:90 91 92 93 94 95 96 97-.....
 00/2C98:98 99 9A 9B 9C 9D 9E 9F-.....
 00/2CA0:A0 A1 A2 A3 A4 A5 A6 A7- !"#\$%&'
 00/2CA8:A8 A9 AA AB AC AD AE AF-()*+,-./

00/2CB0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/2CB8:B8 B9 BA BB BC BD BE BF-89:;<=>?
 00/2CC0:C0 C1 C2 C3 C4 C5 C6 C7-@ABCDEFGH
 00/2CC8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
 00/2CD0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/2CD8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/2CE0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
 00/2CE8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/2CF0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
 00/2CF8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/2D00:00 01 02 03 04 05 06 07-.....
 00/2D08:08 09 0A 0B 0C 0D 0E 0F-.....
 00/2D10:10 11 12 13 14 15 16 17-.....
 00/2D18:18 19 1A 1B 1C 1D 1E 1F-.....
 00/2D20:20 21 22 23 24 25 26 27- !"#%&'
 00/2D28:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
 00/2D30:30 31 32 33 34 35 36 37-01234567
 00/2D38:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/2D40:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/2D48:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
 00/2D50:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
 00/2D58:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/2D60:60 61 62 63 64 65 66 67-'abcdefgh
 00/2D68:68 69 6A 6B 6C 6D 6E 6F-hijklmno
 00/2D70:70 71 72 73 74 75 76 77-pqrstuvw
 00/2D78:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/2D80:80 81 82 83 84 85 86 87-.....
 00/2D88:88 89 8A 8B 8C 8D 8E 8F-.....
 00/2D90:90 91 92 93 94 95 96 97-.....
 00/2D98:98 99 9A 9B 9C 9D 9E 9F-.....
 00/2DA0:A0 A1 A2 A3 A4 A5 A6 A7- !"#%&'
 00/2DA8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/2DB0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/2DB8:B8 B9 BA BB BC BD BE BF-89:;<=>?
 00/2DC0:C0 C1 C2 C3 C4 C5 C6 C7-@ABCDEFGH
 00/2DC8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
 00/2DD0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/2DD8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/2DE0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
 00/2DE8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/2DF0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
 00/2DF8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/2E00:00 01 02 03 04 05 06 07-.....
 00/2E08:08 09 0A 0B 0C 0D 0E 0F-.....
 00/2E10:10 11 12 13 14 15 16 17-.....
 00/2E18:18 19 1A 1B 1C 1D 1E 1F-.....
 00/2E20:20 21 22 23 24 25 26 27- !"#%&'
 00/2E28:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
 00/2E30:30 31 32 33 34 35 36 37-01234567
 00/2E38:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/2E40:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/2E48:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
 00/2E50:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
 00/2E58:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/2E60:60 61 62 63 64 65 66 67-'abcdefgh
 00/2E68:68 69 6A 6B 6C 6D 6E 6F-hijklmno
 00/2E70:70 71 72 73 74 75 76 77-pqrstuvw
 00/2E78:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/2E80:80 81 82 83 84 85 86 87-.....
 00/2E88:88 89 8A 8B 8C 8D 8E 8F-.....

00/2E90:90 91 92 93 94 95 96 97-.....
 00/2E98:98 99 9A 9B 9C 9D 9E 9F-.....
 00/2EA0:A0 A1 A2 A3 A4 A5 A6 A7- !"#%&'
 00/2EA8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/2EB0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/2EB8:B8 B9 BA BB BC BD BE BF-89:;<=>?
 00/2EC0:C0 C1 C2 C3 C4 C5 C6 C7-@ABCDEFGH
 00/2EC8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
 00/2ED0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/2ED8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/2EE0:E0 E1 E2 E3 E4 E5 E6 E7-`abcdefg
 00/2EE8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/2EF0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
 00/2EF8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/2F00:00 01 02 03 04 05 06 07-.....
 00/2F08:08 09 0A 0B 0C 0D 0E 0F-.....
 00/2F10:10 11 12 13 14 15 16 17-.....
 00/2F18:18 19 1A 1B 1C 1D 1E 1F-.....
 00/2F20:20 21 22 23 24 25 26 27- !"#%&'
 00/2F28:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
 00/2F30:30 31 32 33 34 35 36 37-01234567
 00/2F38:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/2F40:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/2F48:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
 00/2F50:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
 00/2F58:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/2F60:60 61 62 63 64 65 66 67-`abcdefg
 00/2F68:68 69 6A 6B 6C 6D 6E 6F-hijklmno
 00/2F70:70 71 72 73 74 75 76 77-pqrstuvw
 00/2F78:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/2F80:80 81 82 83 84 85 86 87-.....
 00/2F88:88 89 8A 8B 8C 8D 8E 8F-.....
 00/2F90:90 91 92 93 94 95 96 97-.....
 00/2F98:98 99 9A 9B 9C 9D 9E 9F-.....
 00/2FA0:A0 A1 A2 A3 A4 A5 A6 A7- !"#%&'
 00/2FA8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/2FB0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/2FB8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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 00/2FC8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
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 00/2FD8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
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 00/2FE8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/2FF0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
 00/2FF8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
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 00/3008:08 09 0A 0B 0C 0D 0E 0F-.....
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 00/3018:18 19 1A 1B 1C 1D 1E 1F-.....
 00/3020:20 21 22 23 24 25 26 27- !"#%&'
 00/3028:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
 00/3030:30 31 32 33 34 35 36 37-01234567
 00/3038:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/3040:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/3048:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
 00/3050:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
 00/3058:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/3060:60 61 62 63 64 65 66 67-`abcdefg
 00/3068:68 69 6A 6B 6C 6D 6E 6F-hijklmno

00/3070:70 71 72 73 74 75 76 77-pqrstuvwxyz
 00/3078:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/3080:80 81 82 83 84 85 86 87-.....
 00/3088:88 89 8A 8B 8C 8D 8E 8F-.....
 00/3090:90 91 92 93 94 95 96 97-.....
 00/3098:98 99 9A 9B 9C 9D 9E 9F-.....
 00/30A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#%&'
 00/30A8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/30B0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/30B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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 00/30C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
 00/30D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/30D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/30E0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
 00/30E8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/30F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvwxyz
 00/30F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/3100:00 01 02 03 04 05 06 07-.....
 00/3108:08 09 0A 0B 0C 0D 0E 0F-.....
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 00/3118:18 19 1A 1B 1C 1D 1E 1F-.....
 00/3120:20 21 22 23 24 25 26 27- !"#%&'
 00/3128:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
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 00/3138:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
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 00/3148:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
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 00/3158:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
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 00/31B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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 00/31C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
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 00/31D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/31E0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
 00/31E8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/31F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvwxyz
 00/31F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/3200:00 01 02 03 04 05 06 07-.....
 00/3208:08 09 0A 0B 0C 0D 0E 0F-.....
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 00/3218:18 19 1A 1B 1C 1D 1E 1F-.....
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 00/3228:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
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 00/3238:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
 00/3240:40 41 42 43 44 45 46 47-@ABCDEFGH
 00/3248:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO

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00/3258:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
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00/3268:68 69 6A 6B 6C 6D 6E 6F-hijklmno
00/3270:70 71 72 73 74 75 76 77-pqrstuvw
00/3278:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
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00/3288:88 89 8A 8B 8C 8D 8E 8F-.....
00/3290:90 91 92 93 94 95 96 97-.....
00/3298:98 99 9A 9B 9C 9D 9E 9F-.....
00/32A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#$%&'
00/32A8:A8 A9 AA AB AC AD AE AF-()*+,-./
00/32B0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
00/32B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
00/32C0:C0 C1 C2 C3 C4 C5 C6 C7-@ABCDEFGH
00/32C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
00/32D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/32D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
00/32E0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
00/32E8:E8 E9 EA EB EC ED EE EF-hijklmno
00/32F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
00/32F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
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00/3308:08 09 0A 0B 0C 0D 0E 0F-.....
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00/3318:18 19 1A 1B 1C 1D 1E 1F-.....
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00/3328:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
00/3330:30 31 32 33 34 35 36 37-01234567
00/3338:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
00/3340:40 41 42 43 44 45 46 47-@ABCDEFGH
00/3348:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
00/3350:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
00/3358:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/3360:60 61 62 63 64 65 66 67-'abcdefgh
00/3368:68 69 6A 6B 6C 6D 6E 6F-hijklmno
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00/3378:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
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00/3388:88 89 8A 8B 8C 8D 8E 8F-.....
00/3390:90 91 92 93 94 95 96 97-.....
00/3398:98 99 9A 9B 9C 9D 9E 9F-.....
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00/33B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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00/33D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/33D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
00/33E0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
00/33E8:E8 E9 EA EB EC ED EE EF-hijklmno
00/33F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
00/33F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
00/3400:00 01 02 03 04 05 06 07-.....
00/3408:08 09 0A 0B 0C 0D 0E 0F-.....
00/3410:10 11 12 13 14 15 16 17-.....
00/3418:18 19 1A 1B 1C 1D 1E 1F-.....
00/3420:20 21 22 23 24 25 26 27- !"#$%&'
00/3428:28 29 2A 2B 2C 2D 2E 2F-()*+,-./

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00/3448:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
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00/3458:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/3460:60 61 62 63 64 65 66 67-'abcdefg
00/3468:68 69 6A 6B 6C 6D 6E 6F-hijklmno
00/3470:70 71 72 73 74 75 76 77-pqrstuvw
00/3478:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
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00/3488:88 89 8A 8B 8C 8D 8E 8F-.....
00/3490:90 91 92 93 94 95 96 97-.....
00/3498:98 99 9A 9B 9C 9D 9E 9F-.....
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00/34A8:A8 A9 AA AB AC AD AE AF-()*+,-./
00/34B0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
00/34B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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00/34C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
00/34D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/34D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
00/34E0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefg
00/34E8:E8 E9 EA EB EC ED EE EF-hijklmno
00/34F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
00/34F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
00/3500:00 01 02 03 04 05 06 07-.....
00/3508:08 09 0A 0B 0C 0D 0E 0F-.....
00/3510:10 11 12 13 14 15 16 17-.....
00/3518:18 19 1A 1B 1C 1D 1E 1F-.....
00/3520:20 21 22 23 24 25 26 27- !"#$%&'
00/3528:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
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00/3538:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
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00/3548:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
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00/3558:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/3560:60 61 62 63 64 65 66 67-'abcdefg
00/3568:68 69 6A 6B 6C 6D 6E 6F-hijklmno
00/3570:70 71 72 73 74 75 76 77-pqrstuvw
00/3578:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
00/3580:80 81 82 83 84 85 86 87-.....
00/3588:88 89 8A 8B 8C 8D 8E 8F-.....
00/3590:90 91 92 93 94 95 96 97-.....
00/3598:98 99 9A 9B 9C 9D 9E 9F-.....
00/35A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#$%&'
00/35A8:A8 A9 AA AB AC AD AE AF-()*+,-./
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00/35B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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00/35C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
00/35D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/35D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
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00/35E8:E8 E9 EA EB EC ED EE EF-hijklmno
00/35F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
00/35F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
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00/3608:08 09 0A 0B 0C 0D 0E 0F-.....

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00/3628:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
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00/3638:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
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00/3648:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
00/3650:50 51 52 53 54 55 56 57-PQRSTUVWXYZ
00/3658:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/3660:60 61 62 63 64 65 66 67-`abcdefg
00/3668:68 69 6A 6B 6C 6D 6E 6F-hijklmno
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00/3688:88 89 8A 8B 8C 8D 8E 8F-.....
00/3690:90 91 92 93 94 95 96 97-.....
00/3698:98 99 9A 9B 9C 9D 9E 9F-.....
00/36A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#$%&'
00/36A8:A8 A9 AA AB AC AD AE AF-()*+,-./
00/36B0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
00/36B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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00/36C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
00/36D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/36D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
00/36E0:E0 E1 E2 E3 E4 E5 E6 E7-`abcdefg
00/36E8:E8 E9 EA EB EC ED EE EF-hijklmno
00/36F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
00/36F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
00/3700:00 01 02 03 04 05 06 07-.....
00/3708:08 09 0A 0B 0C 0D 0E 0F-.....
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00/3718:18 19 1A 1B 1C 1D 1E 1F-.....
00/3720:20 21 22 23 24 25 26 27- !"#$%&'
00/3728:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
00/3730:30 31 32 33 34 35 36 37-01234567
00/3738:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
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00/3748:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
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00/3758:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
00/3760:60 61 62 63 64 65 66 67-`abcdefg
00/3768:68 69 6A 6B 6C 6D 6E 6F-hijklmno
00/3770:70 71 72 73 74 75 76 77-pqrstuvw
00/3778:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
00/3780:80 81 82 83 84 85 86 87-.....
00/3788:88 89 8A 8B 8C 8D 8E 8F-.....
00/3790:90 91 92 93 94 95 96 97-.....
00/3798:98 99 9A 9B 9C 9D 9E 9F-.....
00/37A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#$%&'
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00/37B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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00/37C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
00/37D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/37D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
00/37E0:E0 E1 E2 E3 E4 E5 E6 E7-`abcdefg
00/37E8:E8 E9 EA EB EC ED EE EF-hijklmno

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 00/37F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
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 00/3808:08 09 0A 0B 0C 0D 0E 0F-.....
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 00/3820:20 21 22 23 24 25 26 27- !"#\$%&'
 00/3828:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
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 00/3838:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
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 00/3848:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
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 00/3858:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
 00/3860:60 61 62 63 64 65 66 67-`abcdefg
 00/3868:68 69 6A 6B 6C 6D 6E 6F-hijklmno
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 00/3878:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
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 00/3888:88 89 8A 8B 8C 8D 8E 8F-.....
 00/3890:90 91 92 93 94 95 96 97-.....
 00/3898:98 99 9A 9B 9C 9D 9E 9F-.....
 00/38A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#\$%&'
 00/38A8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/38B0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/38B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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 00/38C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO
 00/38D .D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
 00/38D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
 00/38E0:E0 E1 E2 E3 E4 E5 E6 E7-`abcdefg
 00/38E8:E8 E9 EA EB EC ED EE EF-hijklmno
 00/38F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvwxyz
 00/38F8:F8 F9 FA FB FC FD FE FF-xyz{|}~.
 00/3900:00 01 02 03 04 05 06 07-.....
 00/3908:08 09 0A 0B 0C 0D 0E 0F-.....
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 00/3918:18 19 1A 1B 1C 1D 1E 1F-.....
 00/3920:20 21 22 23 24 25 26 27- !"#\$%&'
 00/3928:28 29 2A 2B 2C 2D 2E 2F-()*+,-./
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 00/3938:38 39 3A 3B 3C 3D 3E 3F-89:;<=>?
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 00/3948:48 49 4A 4B 4C 4D 4E 4F-HIJKLMNO
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 00/3958:58 59 5A 5B 5C 5D 5E 5F-XYZ[\]^_
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 00/3968:68 69 6A 6B 6C 6D 6E 6F-hijklmno
 00/3970:70 71 72 73 74 75 76 77-pqrstuvwxyz
 00/3978:78 79 7A 7B 7C 7D 7E 7F-xyz{|}~.
 00/3980:80 81 82 83 84 85 86 87-.....
 00/3988:88 89 8A 8B 8C 8D 8E 8F-.....
 00/3990:90 91 92 93 94 95 96 97-.....
 00/3998:98 99 9A 9B 9C 9D 9E 9F-.....
 00/39A0:A0 A1 A2 A3 A4 A5 A6 A7- !"#\$%&'
 00/39A8:A8 A9 AA AB AC AD AE AF-()*+,-./
 00/39B0:B0 B1 B2 B3 B4 B5 B6 B7-01234567
 00/39B8:B8 B9 BA BB BC BD BE BF-89:;<=>?
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 00/39C8:C8 C9 CA CB CC CD CE CF-HIJKLMNO

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00/39D0:D0 D1 D2 D3 D4 D5 D6 D7-PQRSTUVWXYZ
00/39D8:D8 D9 DA DB DC DD DE DF-XYZ[\]^_
00/39E0:E0 E1 E2 E3 E4 E5 E6 E7-'abcdefgh
00/39E8:E8 E9 EA EB EC ED EE EF-hijklmno
00/39F0:F0 F1 F2 F3 F4 F5 F6 F7-pqrstuvw
00/39F8:F8 F9 FA FB FC FD FE FF-xyz(|)~.
00/3A00:00 00 08 08 08 08 09 09-.....
00/3A08:09 0A 0A 0A 0B 0B 0C 0C-.....
00/3A10:0C 0C 0D 0D 0E 0E 0F 10-.....
00/3A18:10 10 11 12 12 13 13 14-.....
00/3A20:15 15 16 16 17 18 19 19-.....
00/3A28:1A 1B 1C 1D 1E 1F 1F 20-.....
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00/9718:1D 1D 1E 1F 1F 20 20 20-.....
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00/9730:29 2A 2A 2A 2B 2C 2C 2D-)***+,,-
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00/9768:49 49 4A 4B 4B 4C 4C 4D-IIJKKLLM
00/9770:4D 4D 4E 4F 4F 50 50 51-MMNOOPPQ
00/9778:51 51 52 53 53 54 54 55-QQRSSTTU
00/9780:55 56 56 57 57 57 58 58-UVVVWWXX
00/9788:59 59 5A 5A 5A 5B 5A 5B-YYZZZ[Z[

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00/9790:5B 5C 5C 5C 5D 5D 5D 5D-[\ \ \ ]]]]
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00/97A8:60 60 60 60 60 60 60 60- \ \ \ \ \ \
00/97B0:60 60 60 5F 60 60 5F 5F- \ \ \  \ \
00/97B8:5F 5F 5F 5F 5F 5E 5E 5E-  \ \ \  ^ ^
00/97C0:5E 5E 5E 5D 5D 5D 5C 5C-^ ^ ^ ] ] \ \
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00/97E0:53 52 52 51 51 51 50 50-S R R Q Q Q P P
00/97E8:4F 4E 4E 4D 4D 4C 4C 4B-O N N M M L L K
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00/9838:25 25 24 24 23 23 23 22- & & $ $ # # # "
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